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April 2014

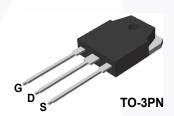
FQA11N90C_F109 N-Channel QFET® MOSFET 900 V, 11.0 A, 1.1 Ω

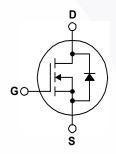
Features

- 11 A, 900 V, $R_{DS(on)} = 1.1 \Omega$ (Max.) @ $V_{GS} = 10 \text{ V}$, $I_D = 5.5 \text{ A}$
- Low Gate Charge (Typ. 60 nC)
- · Low Crss (Typ. 23 pF)
- 100% Avalanche Tested
- · RoHS compliant

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter			FQA11N90C_F109	Unit
V _{DSS}	Drain to Source Voltage			900	V
I _D	Drain Current	- Continuous (T _C = 25°C)		11.0	Α
	Drain Current	- Continuous (T _C = 100°C)		6.9	Α
I _{DM}	Drain Current	- Pulsed	(Note 1)	44.0	Α
V _{GSS}	Gate to Source Voltage			± 30	V
E _{AS}	Single Pulsed Avalanche Energy		(Note 2)	960	mJ
I _{AR}	Avalanche Current		(Note 1)	11.0	Α
E _{AR}	Repetitive Avalanche Energy		(Note 1)	30	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.0	V/ns
P_{D}	Power Dissipation	(T _C = 25°C)		300	W
		- Derate Above 25°C		2.38	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds			300	°C

Thermal Characteristics

Symbol	Parameter	FQA11N90C_F109	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.42	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	40	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA11N90C_F109	FQA11N90C	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	900			V
ΔBV_{DSS} / ΔT_{J}	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		1.02		V/°C
I _{DSS} Zero	Zana Oata Valtana Basin Ourset	V _{DS} = 900 V, V _{GS} = 0 V			10	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 720 V, T _C = 125°C			100	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	3.0		5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 5.5 A		0.91	1.1	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 50 V, I _D = 5.5 A	-	9.0		S
Dynami	ic Characteristics					
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,	\	2530	3290	pF
C _{oss}	Output Capacitance	f = 1.0 MHz		215	280	pF
C _{rss}	Reverse Transfer Capacitance			23	30	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 450 V, I _D = 11.0 A,		60	130	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		130	270	ns
t _{d(off)}	Turn-Off Delay Time			130	270	ns
t _f	Turn-Off Fall Time	(Note 4)		85	180	ns
Q_g	Total Gate Charge	V _{DS} = 720 V, I _D = 11.0 A,	/	60	80	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		13		nC
Q_{gd}	Gate-Drain Charge	(Note 4)		25		nC
Drain-S	Source Diode Characteristics a	nd Maximum Ratings				
Is	Maximum Continuous Drain-Source Diode Forward Current				11.0	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				44.0	Α
V_{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 11.0 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 11.0 A,		1000		ns
Q _{rr}	Reverse Recovery Charge	dl _F / dt = 100 A/μs		17.0		μС

Notes:

^{1.} Repetitive rating : pulse width limited by maximum junction temperature.

^{2.} L = 15 mH, I_{AS} = 11.0 A, V_{DD} = 50 V, R_G = 25 Ω , starting T_J = 25°C.

^{3.} $I_{SD} \le$ 11.0 A, di/dt \le 200 A/ μ s, $V_{DD} \le$ BV $_{DSS}$, starting T_J = 25°C.

^{4.} Essentially independent of operating temperature.

Typical Characteristics

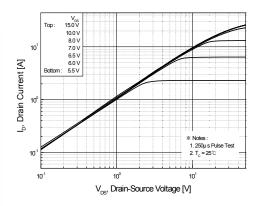


Figure 1. On-Region Characteristics

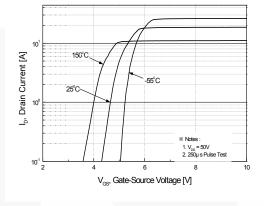


Figure 2. Transfer Characteristics

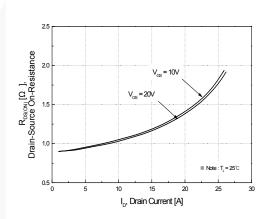


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

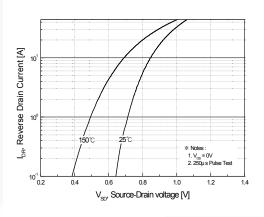


Figure 4. Body Diode Forward Voltage Variation with Source Current

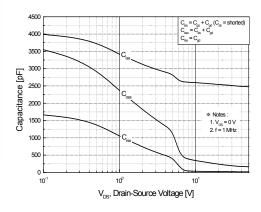


Figure 5. Capacitance Characteristics

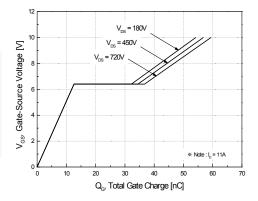
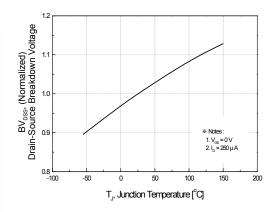


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)



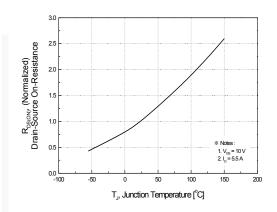
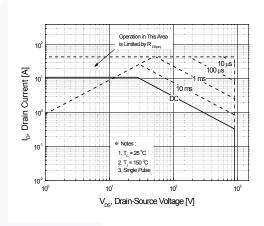


Figure 7. Breakdown Voltage Variation





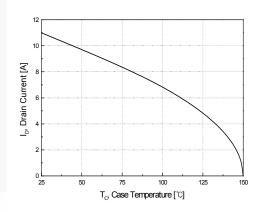


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs Case Temperature

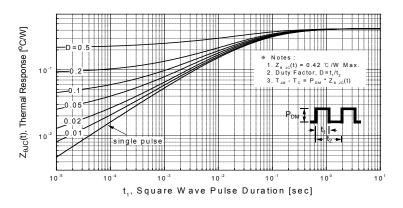


Figure 11. Transient Thermal Response Curve

Figure 12. Gate Charge Test Circuit & Waveform

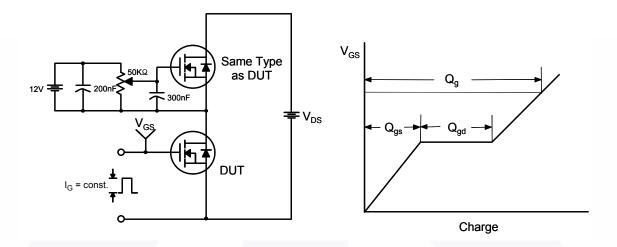


Figure 13. Resistive Switching Test Circuit & Waveforms

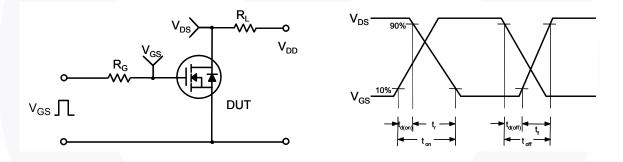
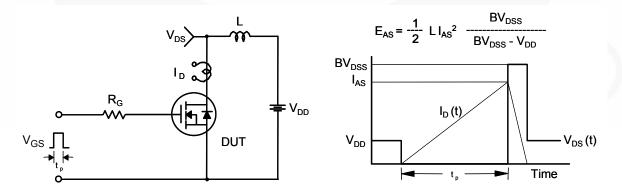


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



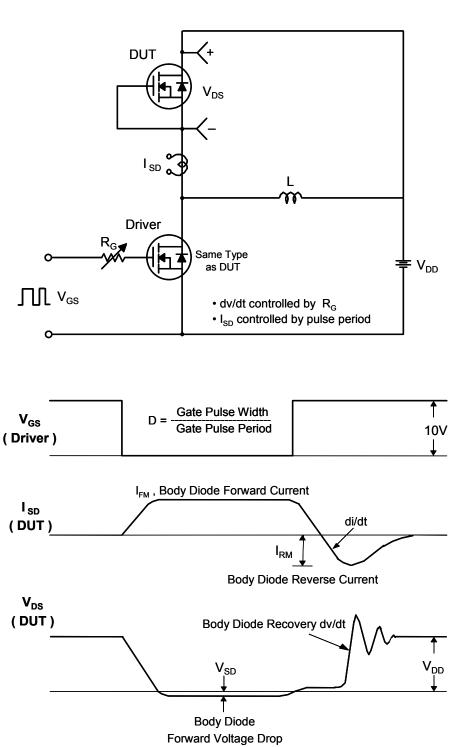


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

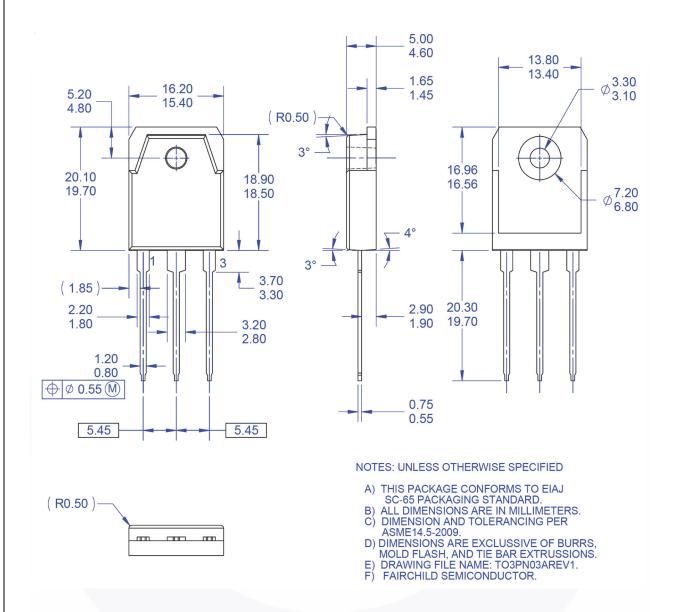


Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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