

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI[®]123

DFLS130L

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI[®]123 •
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- **Terminal Connections: Cathode Band**
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (C3)
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	V
RMS Reverse Voltage	V _{R(RMS)}	21	V
Average Forward Current @ T _T = 121°C	I _{F(AV)}	1.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	50	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 1)	PD	1.67	W
Power Dissipation (Note 2)	PD	556	mW
Thermal Resistance Junction to Ambient (Note 1)	R _{0JA}	60	°C/W
Thermal Resistance Junction to Ambient (Note 2)	R ₀ JA	180	°C/W
Thermal Resistance Junction to Soldering (Note 3)	R _{θJS}	10	°C/W
Operating Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T _{STG}	-40 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	30	_	_	V	I _R = 1.0mA
		_	0.210			I _F = 0.1A
Forward Voltage	VF	_	0.310	_	V	$I_{F} = 1.0A$
			0.328	0.36		I _F = 1.5A
Leakage Current (Note 5)	1-	_	0.260		mA	$V_{R} = 5V, T_{A} = 25^{\circ}C$
Leakage Cullent (Note 5)	IR	_	—	1.0	IIIA	$V_{R} = 30V, T_{A} = 25^{\circ}C$
Total Capacitance	CT		76	_	pF	$V_{R} = 10V, f = 1.0MHz$

Notes: Part mounted on 2"x2" GETEK board with 1"x1" copper pad, 25% anode, 75% cathode. T_A = 25°C.

2. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

3. Theoretical Reus calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html. 5.

Short duration pulse test used to minimize self-heating effect.

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= -40°C

20

25

30

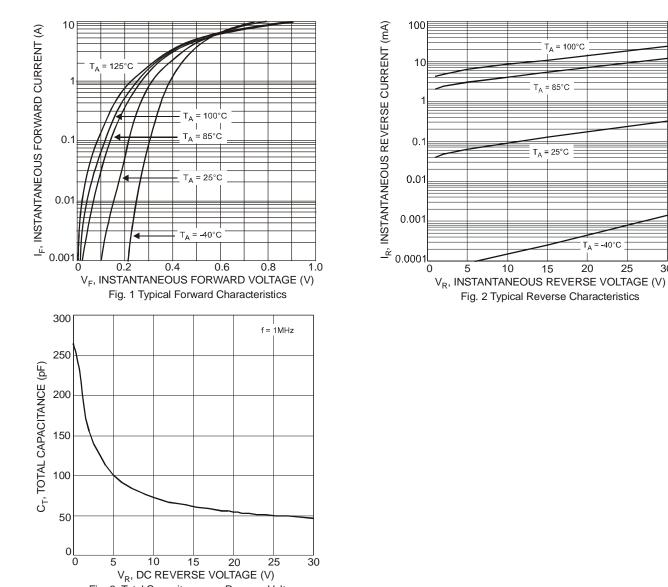


Fig. 3 Total Capacitance vs. Reverse Voltage

Ordering Information (Note 6)

Part Number	Case	Packaging
DFLS130L-7	PowerDI [®] 123	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

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	F03	ΥN	

F03 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: R = 2004) M = Month (ex: 9 = September)

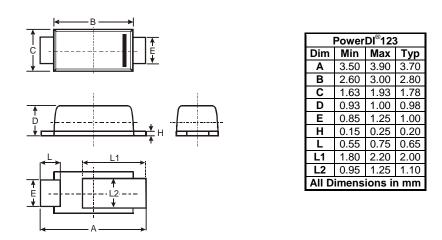
Date	Code	Key

Year	2004	20	05	2006	2007	20	08	2009	2010	20	11	2012
Code	R	5	S	Т	U	Ň	V	W	Х	Ň	Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

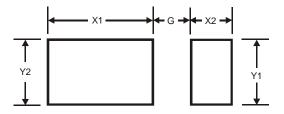
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Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4



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