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FAN54151 High Current Charging Switch with Protection

Features

- Integrated Back-to-Back Common Source
 N-channel MOSFETs with combined R_{ON} = 17 mΩ
- Over-Voltage Input Protection to +20 V
- Hardware-based Safety and Protection for:
 - Input Over-Voltage Protection and Lockout
 - Input Under-Voltage Lockout
 - Fast and Slow Output Over-Voltage Protection
 - Over-Current Protection with Selectable Limits
 - Safe Operating Temperature
 - Charger Detach Protection
- True Reverse Current Blocking (TRCB)
- Fully Integrated I²C Slave with Configurable Address
- Configurable Host Interrupts

Applications

- High Current Battery Charging for:
 - Mobile Devices
 - Tablets

Description

The FAN54151 is a low loss, high current, I²C controlled switch.

Built in protection monitors the ICs input and output for over- and under-voltage as well as over- and under-current. The system protection is provided by controlling the gate voltages of a pair of N-channel MOSFETs to ensure that the output stays within a safe operating range. If a fault is detected, the FAN54151 will turn off the N-channel FETs and notify the host system via configurable interrupts.

Additionally, the FAN54151 includes an integrated temperature sensor and battery cell voltage monitor that provides additional protection.

The FAN54151 utilizes a 20-Bump, 0.5 mm pitch WLCSP package.

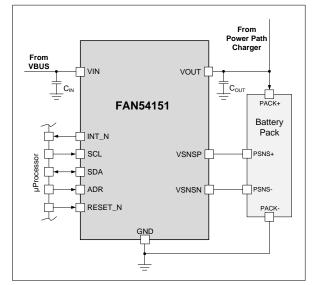
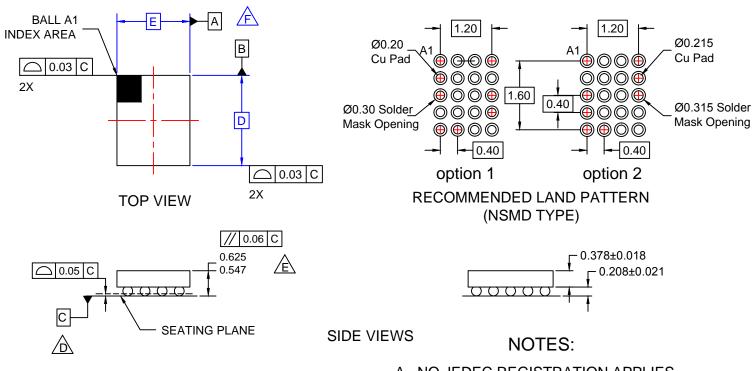


Figure 1. Typical Application



⊕ 0.005 M C A B 1.20 Ø0.260±0.02 0.40 20X Ε 0000 D 0001.60 С \oplus \bigcirc \bigcirc \bigcirc $-(Y) \pm 0.018$ 0.40 \oplus OiO \oplus 2 3 (X) ±0.018

BOTTOM VIEW

- A. NO JEDEC REGISTRATION APPLIES.
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCE PER ASMEY14.5M, 2009.
- DATUM C IS DEFINED BY THE SPHERICAL CROWNS OF THE BALLS.
- PACKAGE NOMINAL HEIGHT IS 586 MICRONS ±39 MICRONS (547-625 MICRONS).
- FOR DIMENSIONS D, E, X, AND Y SEE PRODUCT DATASHEET.
- G. DRAWING FILNAME: MKT-UC020AArev4.







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Definition of Terms

Definition of Terms		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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