General Description

The MAX4236/MAX4237 are high-precision op amps that feature an exceptionally low offset voltage and offset voltage temperature coefficient without using any chopper techniques. The MAX4236 and MAX4237 have a typical large-signal, open-loop voltage gain of 120dB. These devices have an ultra-low input-bias current of 1pA. The MAX4236 is unity-gain stable with a gainbandwidth product of 1.7MHz, while the MAX4237 is stable for closed-loop gains greater than 5V/V with a gain-bandwidth product of 7.5MHz. Both devices have a shutdown function in which the quiescent current is reduced to less than 0.1µA, and the amplifier output is forced into a high-impedance state.

The input common-mode range of the MAX4236/ MAX4237 extends below the negative supply range, and the output swings Rail-to-Rail®. These features make the amplifiers ideal for applications with +3V or +5V single power supplies. The MAX4236/MAX4237 are specified for the extended temperature range (-40°C to +85°C) and are available in tiny SOT23, µMAX, and SO packages. For greater accuracy, the A grade µMAX and SO packages are tested to guarantee 20µV (max) offset voltage at +25°C and less then 2µV/°C drift.

Applications

Strain Gauges

Piezoelectric Sensors

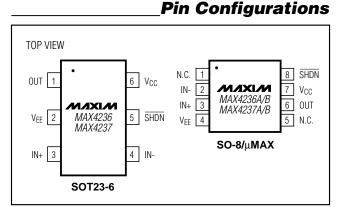
Thermocouple Amplifiers

Electrochemical Sensors

Battery-Powered Instrumentation

Instrumentation Amplifiers

Rail-to-Rail is a registered trademark of Nippon Motorola, Inc.



Features

- Ultra-Low Offset Voltage 20uV (max) at +25°C (Grade A) 50µV (max) at +25°C (Grade B, 6-Pin SOT23)
- Ultra-Low Offset Voltage Drift 2µV/°C (max) (Grade A) 4.5µV/°C (max) (Grade B, 6-Pin SOT23) 5.5µV/°C (max) (6-Pin SOT23)
- Ultra-Low 1pA Input Bias Current
- High Open-Loop Voltage Gain: 110dB (min) (R_L = 100kΩ)
- Compatible with +3V and +5V Single-Supply Power Systems
- Ground Sensing: Input Common-Mode Range **Includes Negative Rail**
- * Rail-to-Rail Output Swing into a 1kΩ Load
- 350µA Quiescent Current
- Gain-Bandwidth Product 1.7MHz (MAX4236, A_V = 1V/V) 7.5MHz (MAX4237, Av = 5V/V)
- 200pF Capacitive Load Handling Capability
- Shutdown Mode: 0.1µA Quiescent Current, Places Output in a High-Impedance State
- Available in Space-Saving SOT23 and µMAX Packages

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4236EUT-T	-40°C to +85°C	6 SOT23-6
MAX4236AEUA	-40°C to +85°C	8 µMAX
MAX4236BEUA	-40°C to +85°C	8 µMAX
MAX4236AESA	-40°C to +85°C	8 SO
MAX4236BESA	-40°C to +85°C	8 SO
MAX4237EUT-T	-40°C to +85°C	6 SOT23-6
MAX4237AEUA	-40°C to +85°C	8 µMAX
MAX4237BEUA	-40°C to +85°C	8 µMAX
MAX4237AESA	-40°C to +85°C	8 SO
MAX4237BESA	-40°C to +85°C	8 SO

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

6-Pin SOT23-6 (derate 8.7mW/°C above +70°C)	696mW
8-Pin µMAX (derate 4.5mW/°C above +70°C)	362mW
8-Pin SO (derate 5.9mW/°C above +70°C)	471mW

Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS (SO-8 and µMAX-8)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}$ C.) (Note 1)

PARAMETER	SYMBOL	CON	DITIONS	MIN	ТҮР	МАХ	UNITS
Supply Voltage Range	V _{CC}	Guaranteed by the F	PSRR test	2.4		5.5	V
		$V_{CC} = +5V$	In normal mode		350	440	
Quiaccent Supply Current		ACC = +2A	In shutdown mode		0.1	2	
Quiescent Supply Current	Icc	$V_{CC} = +3V$	In normal mode		350	440	μA
		VCC = +3V	In shutdown mode		0.1	2	
		$V_{CC} = +5V,$	T _A = +25°C		±5	±20	
Input Offset Voltage	Vos	Grade A	$T_A = T_{MIN}$ to T_{MAX}			±150	μV
input Onset voltage	VUS	$V_{CC} = +5V,$	T _A = +25°C		±5	±50	μv
		Grade B	$T_A = T_{MIN}$ to T_{MAX}			±340	
Input Offset Voltage Temperature	TCVOS	$V_{CC} = +5V$	Grade A		±0.6	±2	µV/°C
Coefficient	10103	(Note 3)	Grade B		±0.6	±4.5	μν, Ο
Input Bias Current	Ι _Β	(Note 2)			±1	±500	рА
Input Offset Current	los	(Note 2)			±1		рА
Input Resistance	RIN	Differential or commo	on mode		1000		MΩ
Input Common-Mode Voltage	V _{CM}	Guaranteed by the C	CMRR test	-0.15		V _{CC} - 1.2	V
		$V_{CC} = +5V;$ -0.15V $\leq V_{CM} \leq$	$T_A = +25^{\circ}C$	84	102		
Common-Mode Rejection Ratio	CMRR	(V _{CC} - 1.2V)	$T_A = T_{MIN}$ to T_{MAX}	80			dB
		V _{CC} = +3.0V; -0.15V ≤ V _{CM} ≤	$T_A = +25^{\circ}C$	82	102		0.2
		(V _{CC} - 1.2V)	$T_A = T_{MIN}$ to T_{MAX}	78			
Power-Supply Rejection Ratio	DODD	$V_{CC} = +2.4V$ to	$T_A = +25^{\circ}C$	97	120		dB
	PSRR	+5.5V	$T_A = T_{MIN}$ to T_{MAX}	95			uв

ELECTRICAL CHARACTERISTICS (SO-8 and µMAX-8) (continued)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}\text{C.}$) (Note 1)

PARAMETER	SYMBOL CONDITIONS		MIN	ТҮР	MAX	UNITS		
		$V_{CC} = +5V, R_L$		$Ok\Omega, V_{OUT} = 0$ (V _{CC} - 50mV)	110	128		
		connected to V _{CC} /2, T _A = +25°C		Ω, V _{OUT} = (V _{CC} - 0.3V)	105	114		
		$V_{CC} = +5V, R_L$ connected to		$0k\Omega$, V _{OUT} = (V _{CC} - 50mV)	110			
		V _{CC} /2, T _A = T _{MIN} to T _{MAX}	R _L = 1ks V _{OUT} = to (V _{CC}	0.15V	100			
Large-Signal Voltage Gain	Avol	$V_{CC} = +3V, R_L$		$Ok\Omega, V_{OUT} = 0$ (V _{CC} - 50mV)	110	128		dB
		connected to V _{CC} /2, T _A = +25°C	R _L = 1ks V _{OUT} = to (V _{CC}	0.15V	100	114		-
		V_{CC} = +3V, R _L connected to $V_{CC}/2$, T_A = T _{MIN} to T _{MAX}		$Ok\Omega, V_{OUT} = 0$ (V _{CC} - 50mV)	105			
			R _L = 1ks V _{OUT} = to (V _{CC}	0.15V	95			
		V _{CC} = +5V,		V _{CC} - V _{OH}		2	10	
	Maria	R_L connected to V_C R_L = 100k Ω	CC/2,	V _{OL} - V _{EE}		3	10	
Output Voltage Swing	Vout	$V_{CC} = +5V,$	20/2	V _{CC} - V _{OH}		150	250	- mV
		R_L connected to V _{CC} /2, $R_L = 1k\Omega$		V _{OL} - V _{EE}		50	100	
Output Short-Circuit Current	IOUT(SC)	Shorted to V _{EE}				10		mA
	1001(30)	Shorted to V _{CC}		1		30		
Gain-Bandwidth Product	GBWP	$R_{L} = \infty, C_{L} = 5pF$ MAX4236 MAX4237		-		1.7 7.5		MHz
Slew Rate	SR	V _{CC} = +5V, V _{OUT} = 4V step MAX4236 MAX4237			0.3 1.3		V/µs	
Settling Time	ts	V _{OUT} settling to within MAX4236		1		1		μs
Total Harmonic Distortion	THD	$f = 5kHz, V_{OUT} = 2'$ $R_L = 10k\Omega$	Vp-p, V _{CC}			0.001		%

ELECTRICAL CHARACTERISTICS (SO-8 and µMAX-8) (continued)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}\text{C.}$) (Note 1)

PARAMETER	SYMBOL	CONDITION	S	MIN	ТҮР	MAX	UNITS
Input Capacitance	CIN	f = 100kHz			7.5		рF
Input Voltage Noise Density	en	f = 1kHz			14		nV/√Hz
Input Noise Voltage	e _{np-p}	f = 0.1Hz to 10Hz			0.2		µVр-р
Connective Load Stability	CLOAD	No sustained oscillations	MAX4236		200		рЕ
Capacitive Load Stability	CLOAD	NO SUSTAILIEU OSCIIIATIONS	MAX4237		200	pF	
Shutdown Mode Output Leakage	IOUT(SH)	Device in shutdown mode (\overline{S} V _{OUT} = 0 to V _{CC}	SHDN = V _{EE})		±0.01	±1.0	μA
SHDN Logic Low	VIL					0.3 × V _{CC}	V
SHDN Logic High	VIH			0.7 × V _{CC}			V
SHDN Input Current		$\overline{\text{SHDN}} = V_{\text{EE}} \text{ or } V_{\text{CC}}$			1	3	μA
Shutdown Delay Time	t(SH)	$R_L = 1k\Omega$			1		μs
Shutdown Recovery Time	t(EN)	$R_L = 1k\Omega$			4		μs

ELECTRICAL CHARACTERISTICS (SOT23-6)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}$ C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	Vcc	Guaranteed by the PS	SRR test	2.4		5.5	V
			In normal mode		350	440	
Quieseent Querely Querent	1	$V_{CC} = +5V$	In shutdown mode		0.1	2	
Quiescent Supply Current	Icc		In normal mode		350	440	μA
		$V_{CC} = +3V$	In shutdown mode		0.1	2	
			T _A = +25°C		±5	±50	
Input Offset Voltage	Vos	$V_{CC} = +5V$	$T_A = T_{MIN}$ to T_{MAX}			±600	μV
Input Offset Voltage Temperature Coefficient (Note 2)	TCV _{OS}	$V_{CC} = +5V$		±0.6	±5.5	µV/°C	
Input Bias Current	Ι _Β	(Note 2)			±1	±500	рΑ
Input Offset Current	IOS	(Note 2)			±1		рΑ
Input Resistance	RIN	Differential or commo	n mode		1000		MΩ
Input Common-Mode Voltage	VCM	Guaranteed by the CMRR test		-0.15		V _{CC} - 1.2	V
		V _{CC} = +5V, -0.15V	T _A = +25°C	82	102		
		$\leq V_{CM} \leq (V_{CC} - 1.2V)$	$T_A = T_{MIN}$ to T_{MAX}	80			dB
Common-Mode Rejection Ratio	CMRR	VCC = +3.0V; -0.15V	T _A = +25°C	82	102		
		\leq V _{CM} \leq (V _{CC} - 1.2V)		78			

ELECTRICAL CHARACTERISTICS (SOT23-6) (continued)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}$, unless otherwise noted. Typical values are at $V_{CC} = +5V$ and $T_A = +25^{\circ}\text{C.}$) (Note 1)

PARAMETER	SYMBOL	CO	NDITIO	NS	MIN	ТҮР	MAX	UNITS
	5055	$V_{CC} = +2.4V$ to	T _A =	+25°C	97	120		
Power-Supply Rejection Ratio	PSRR	+5.5V	T _A =	T _{MIN} to T _{MAX}	95			dB
		V _{CC} = +5V, R _L connected to	Vout	100kΩ, r = 15mV to - 50mV)	110	128		
		V _{CC} /2, T _A = +25°C	Vout	1kΩ, ϝ = 0.15V ϝcc - 0.3V)	100	114		
		$V_{CC} = +5V, R_L$ connected to		100kΩ, V _{OUT} = V to (V _{CC} - 50mV)	110			
Large-Signal Voltage Gain	Avol	V _{CC} /2, TA = T _{MIN} to T _{MAX}	Vout	1kΩ, r = 0.15V to - 0.3V)	95			dB
Large-Signal Voltage Gain	, WOL	V _{CC} = +3V, R _L connected to	Vout	100kΩ, r = 15mV to - 50mV)	110	128		gr
		$V_{CC}/2$, $T_A = +25^{\circ}C$	Vout	1kΩ, r = 0.15V to - 0.3V)	100	114		
		$V_{CC} = +3V, R_L$ connected to $V_{CC}/2,$ $T_A = T_{MIN}$ to T_{MAX}	Vout	100kΩ, r = 15mV to - 50mV)	105			
			Vout	1kΩ, r = 0.15V to - 0.3V)	95			
		$V_{CC} = +5V$, R _L connected to V	00/2	V _{CC} - V _{OH}		2	10	
		$R_L = 100 k\Omega$	0012,	V _{OL} - V _{EE}		3	10	
Output Voltage Swing	Vout	$V_{CC} = +5V,$	o.o./0	V _{CC} - V _{OH}		150	250	- mV
		R_L connected to V_0 $R_L = 1k\Omega$	CC/2,	V _{OL} - V _{EE}		50	100	
		Shorted to VEE			10		_	
Output Short-Circuit Current	IOUT(SC)	Shorted to V _{CC}				30		mA
				MAX4236	1	1.7		
Gain-Bandwidth Product	GBWP	R _L = ∞, C _L = 15pF		MAX4237		7.5		MHz
Slew Rate	SR	$V_{\rm CC} = +5V,$		MAX4236		0.3		V/µs
JEW HALE	011	V _{OUT} = 4V step		MAX4237		1.3		



ELECTRICAL CHARACTERISTICS (SOT23-6) (continued)

 $(V_{CC} = +2.4V \text{ to } +5.5V, V_{EE} = 0, V_{CM} = 0, V_{OUT} = V_{CC}/2, R_L = 100 \text{k}\Omega \text{ to } V_{CC}/2, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +5V \text{ and } T_A = +25^{\circ}\text{C.}) \text{ (Note 1)}$

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Cottling Time	to		MAX4236		1		
Settling Time	ts	V _{OUT} settling to within 0.01%	MAX4237		1		μs
Total Harmonic Distortion	THD	f = 5kHz, V_{OUT} = 2Vp-p, V_{CC} = +5V R _L = 10k Ω			0.001		%
Input Capacitance	CIN	f = 100 kHz			7.5		рF
Input Voltage Noise Density	en	f = 1kHz			14		nV/√Hz
Input Noise Voltage	e _{np-p}	f = 0.1Hz to $10Hz$	f = 0.1Hz to $10Hz$		0.2		µVр-р
Capacitive Load Stability	C	No sustained oscillations	MAX4236		200		рF
Capacitive Load Stability	C _{LOAD}	NO SUSTAILIEU OSCIIIATIONS	MAX4237		200		μr
Shutdown Mode Output Leakage	IOUT(SH)	Device in shutdown mode ($\overline{SHDN} = V_{EE}$) V _{OUT} = 0 to V _{CC}			±0.01	±1.0	μA
SHDN Logic Low	VIL					0.3 × V _{CC}	V
SHDN Logic High	VIH			0.7 × V _{CC}			V
SHDN Input Current		$\overline{\text{SHDN}} = \text{V}_{\text{EE}} \text{ or } \text{V}_{\text{CC}}$			1	3	μA
Shutdown Delay Time	t(SH)	$R_L = 1k\Omega$			1		μs
Shutdown Recovery Time	^t (EN)	$R_L = 1k\Omega$			4		μs

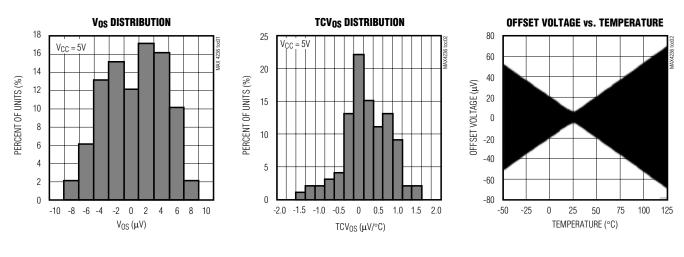
Note 1: All devices are 100% production tested at $T_A = +25$ °C; all specifications over temperature are guaranteed by design, unless otherwise specified.

Note 2: Guaranteed by design, not production tested.

Note 3: Maxim specification limits for the temperature coefficient of the offset voltage (TCV_{OS}) are 100% tested for the A-grade, 8pin SO and µMAX packages.

Typical Operating Characteristics

(V_{CC} = +5V, V_{EE} = 0, V_{CM} = V_{CC}/2, R_L = 100 k\Omega to V_{CC}/2, T_A = +25°C, unless otherwise noted.)

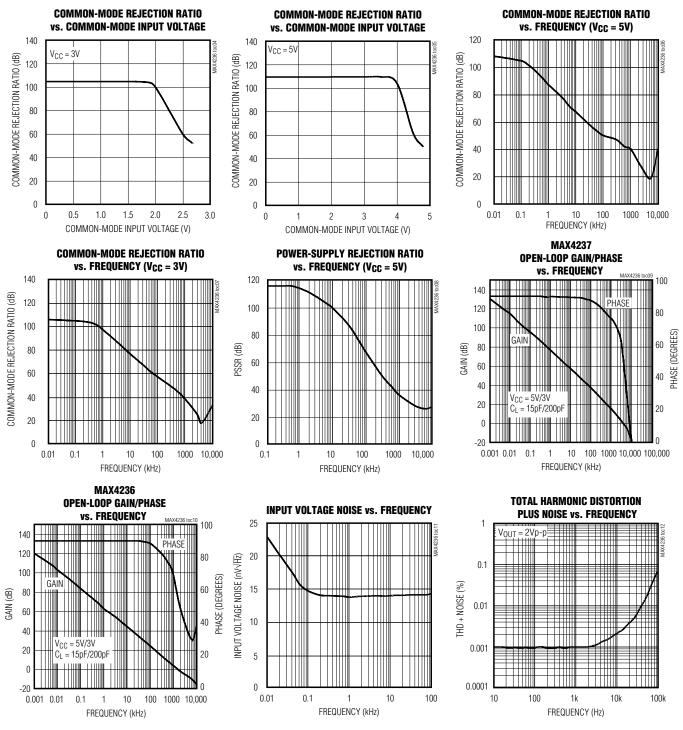




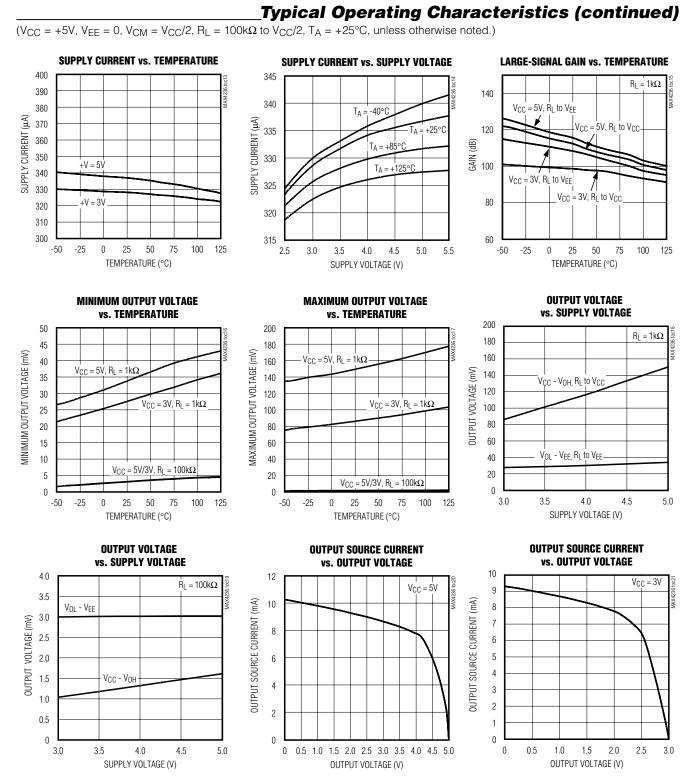
Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{EE} = 0, V_{CM} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2, T_A = +25^{\circ}C$, unless otherwise noted.)

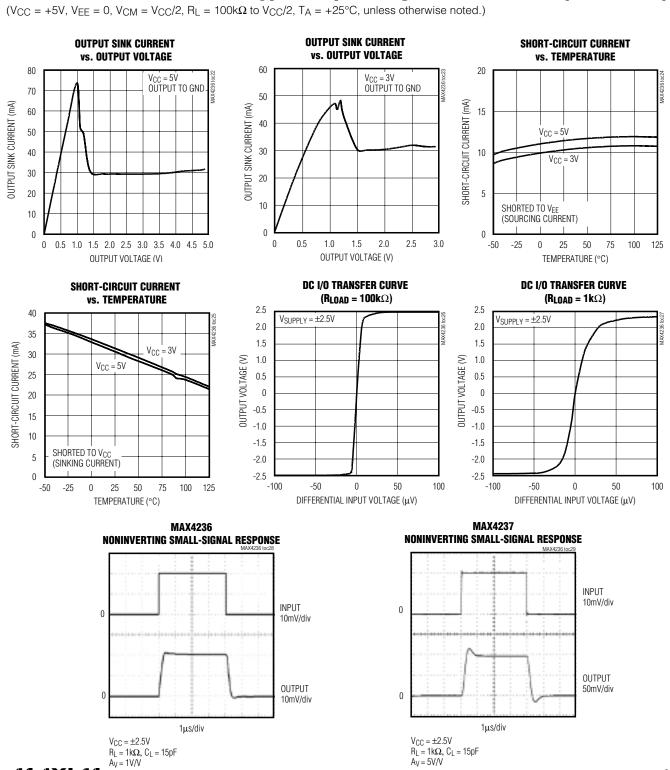
MIXIM



MAX4236/MAX4237



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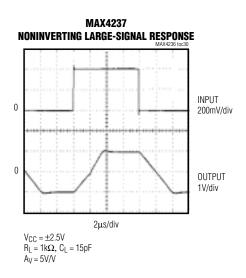


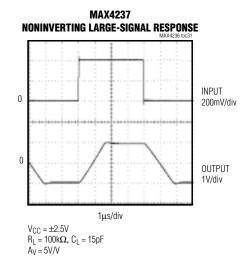
__Typical Operating Characteristics (continued)

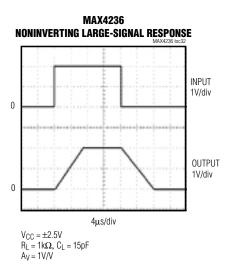
MAX4236/MAX423'

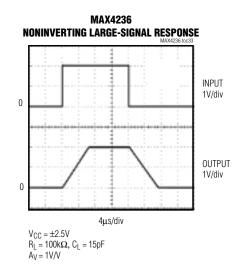
_Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, V_{EE} = 0, V_{CM} = V_{CC}/2, R_L = 100k\Omega$ to $V_{CC}/2, T_A = +25^{\circ}C$, unless otherwise noted.)









_Pin Description

Р	PIN		FUNCTION
SOT23	SO/μΜΑΧ		
1	6	OUT	Amplifier Output
2	4	V _{EE}	Negative Power Supply. Bypass with a $0.1\mu F$ capacitor to ground. Connect to GND for single-supply operation.
3	3	IN+	Noninverting Amplifier Input
4	2	IN-	Inverting Amplifier Input
5	8	SHDN	Shutdown Input. Do not leave floating. Connect to V_{CC} for normal operation or GND to enter the shutdown mode.
6	7	V _{CC}	Positive Supply Input. Bypass with a $0.1 \mu F$ capacitor to ground.
	1, 5	N.C.	No Connection. Not internally connected.

Detailed Description

The MAX4236/MAX4237 are high-precision op amps with a CMOS input stage and an excellent set of DC and AC features. The combination of tight maximum voltage offset, low offset tempco and very low input current make them ideal for use in high-precision DC circuits. They feature low-voltage operation, low-power consumption, high-current drive with rail-to-rail output swing and high-gain bandwidth product.

High Accuracy

The MAX4236/MAX4237 maximum input offset voltage is 20µV (5µV, typ) for grade A version and 50µV for grade B version at +25°C. The maximum temperature coefficient of the offset voltage for grade A and B are guaranteed to be 2µV/°C and 4.5µV/°C respectively. The parts have an input bias current of 1pA. Noise characteristics are 14nV/ \sqrt{Hz} , and a low frequency noise (0.1Hz to 10Hz) of 0.2µVp-p. The CMRR is 102dB, and the PSRR is 120dB. The combination is what is necessary for the design of circuits to process signals while keeping high signal-to-noise ratios, as in stages preceding high-resolution converters, or when they are produced by sensors or transducers generating very small outputs.

Rail-to-Rail Outputs, Ground-Sensing Input The input common-mode range extends from (V_{EE} -0.15V) to (V_{CC} - 1.2V) with excellent common-mode rejection. Beyond this range, the amplifier output is a nonlinear function of the input, but does not undergo phase reversal or latch-up (see *Typical Operating Characteristics*). The output swings to within 150mV of the power-supply rails with a $1k\Omega$ load. The input ground sensing and the rail-to-rail output substantially increase the dynamic range.

Power-Up and Shutdown Mode

The MAX4236/MAX4237 have a shutdown option. When the shutdown pin (SHDN) is pulled low, the supply current drops to 0.1μ A, and the amplifiers are disabled with the output in a high-impedance state. Pulling SHDN high enables the amplifiers. The turn-on time for the amplifiers to come out of shutdown is 4µs.

Applications Information

As described above, the characteristics of the MAX4236/MAX4237 are excellent for high-precision/ accuracy circuitry, and the high impedance, low-current, low-offset, and noise specifications are very attractive for piezoelectric transducers applications. In these applications, the sensors generate an amount of electric charge proportional to the changes in the mechanical stress applied to them. These charges are transformed into a voltage proportional to the applied force by injecting them into a capacitance and then amplifying the resulting voltage. The voltage is an inverse function of the capacitance into which the charges generated by the transducer/ sensor are injected. This capacitance and the resistance that discharges it, define the low-frequency response of the circuit. It is desirable, once the preferred low-frequency response is known, to maintain the capacitance as low as possible, because the amount of necessary upstream amplification (and the signal-to-noise ratio deterioration) is directly proportional to the capacitance value. The MAX4236/MAX4237 high-impedance, low-

current, low-noise inputs allow a minimum of capacitance to be used.

Piezoresistive transducers applications require many of the same qualities. For those applications the MAX4236/MAX4237 high CMRR, PSRR, and offset stability are also a good match.

A typical application for a piezoresistive transducer instrumentation amplifier design using the MAX4236/MAX4237 is shown in the *Typical Application Circuit*.

In general, the MAX4236/MAX4237 are good components for any application in which an amplifier with an almost zero input current is required, including highprecision, long time-constant integrators and electrochemical sensors.

Power Supplies

The MAX4236/MAX4237 can operate from a single +2.4V to +5.5V power supply, or from $\pm 1.2V$ to $\pm 2.75V$ power supplies. The power supply pin(s) must be bypassed to ground with a 0.1µF capacitor as close to the pin as possible.

Layout and Physical Design

A good layout improves performance by decreasing the amount of parasitic and stray capacitance, inductance and resistance at the amplifier's inputs, outputs, and power-supply connections. Since parasitics might be unavoidable, minimize trace lengths, resistor leads, and place external components as close to the pins as possible.

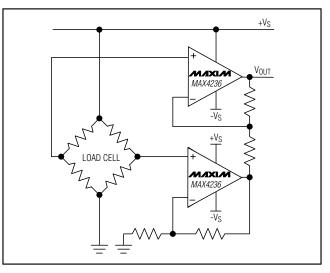
In high impedance, low input current applications, input lines guarding and shielding, special grounding, and other physical design and layout techniques, are mandatory if good results are expected.

The negative effects of crosstalk, EMI and other forms of interference and noise (thermal, acoustic, etc.) must be accounted for and prevented beforehand for good performance in the type of sensitive circuitry in which the MAX4236/MAX4237 are likely to be used.

PART	GRADE	MINIMUM STABLE GAIN	TOP MARK
MAX4236EUT	_	1	AAUV
MAX4236AEUA	А	1	
MAX4236BEUA	В	1	—
MAX4236AESA	А	1	—
MAX4236BESA	В	1	—
MAX4237EUT	_	5	AAUW
MAX4237AEUA	А	5	—
MAX4237BEUA	В	5	—
MAX4237AESA	А	5	—
MAX4237BESA	В	5	—

Typical Application Circuit

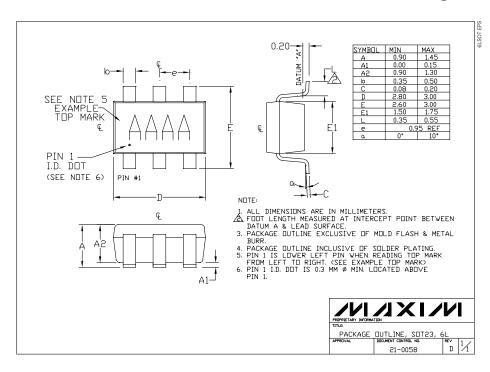
Selector Guide

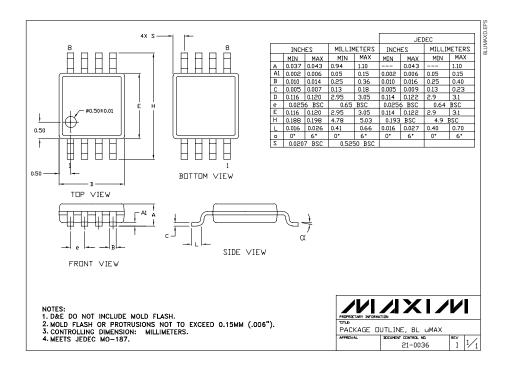


Chip Information

TRANSISTOR COUNTS: 224 PROCESS: BICMOS

Package Information





MAX4236/MAX4237

Package Information (continued) ЕΗ F 0°-8° B INCHES MILLIMETERS INCHES MILLIMETERS MS012 MIN MAX MIN MAX MIN МАХ MIN MAX Ν 5.00 0.053 0.069 1.35 1.75 D 0.189 0.197 4.80 8 А А A1 0.004 | 0.010 0.10 0.25 0.337 0.344 8.55 8.75 В D 14 B 0.014 0.019 0.35 0.49 D 0.386 0.394 9.80 10.00 16 С 0.007 0.010 0.19 0.25 С 0.050 ρ 1.27 NDTES: Ε 0.150 0.157 3.80 4.00 D&E DO NOT INCLUDE MOLD FLASH 2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006") 3. LEADS TO BE COPLANAR WITHIN 5.80 6.20 H 0.228 0.244 0.25 0.50 h 0.010 0.020 .102mm (.004") L 0.016 0.050 0.40 1.27 CONTROLLING DIMENSION: MILLIMETER MEETS JEDEC MS012-XX AS SHOWN Λ 5 IN ABOVE TABLE N = NUMBER OF PINS 6. ₽ACKAGE FAMILY DUTLINE: SDIC .150″ 21-0041 A 120 SAN GABRIEL DR SUMNYVALE CA 94086 FAX (408) 737 7194 PROPRIETARY INFORMATION

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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MAX4236/MAX4237

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