

V _{CES}	650V
I _{C(100°C)}	8A
V _{CE(sat) (Typ.)}	1.65V
P _D	94W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

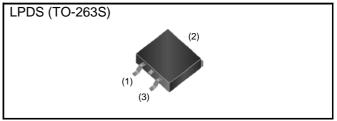
General Inverter

UPS

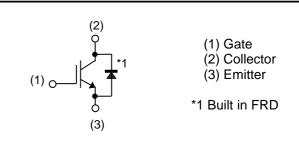
Power Conditioner

Welder

Outline



Inner Circuit



Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Type	Tape Width (mm)	24
Туре	Basic Ordering Unit (pcs)	1,000
	Taping Code	TL
	Marking	RGT16NS65D

●Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι _C	16	А
Collector Current	$T_{\rm C} = 100^{\circ}{\rm C}$	Ι _C	8	А
Pulsed Collector Current		I _{CP} *1	24	А
Diode Forward Current	$T_{C} = 25^{\circ}C$	١ _F	16	А
Diode Forward Current	$T_{C} = 100^{\circ}C$	١ _F	8	А
Diode Pulsed Forward Current		I _{FP} ^{*1}	24	А
Power Dissipation	$T_{C} = 25^{\circ}C$	P _D	94	W
Power Dissipation	$T_{\rm C} = 100^{\circ}{\rm C}$	P _D	47	W
Operating Junction Temperatu	ire	Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C
*1 Pulse width limited by T		•	·	

*1 Pulse width limited by T_{jmax.}

Thermal Resistance

Parameter	Symbol	Values			Unit
	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.58	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	3.60	°C/W

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions		Unit		
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V
Collector Cut - off Current	I _{CES}	V _{CE} = 650V, V _{GE} = 0V	-	-	10	μA
Gate - Emitter Leakage Current	I _{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA
Gate - Emitter Threshold Voltage	V _{GE(th)}	$V_{CE} = 5V, I_{C} = 5.5mA$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 8A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.65 2.15	2.1 -	V

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•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

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Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	450	-		
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	21	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	8	-		
Total Gate Charge	Q_g	V _{CE} = 300V	-	21	-		
Gate - Emitter Charge	Q_{ge}	I _C = 8A	-	6	-	nC	
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	8	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 8$ A, $V_{\rm CC} = 400$ V	-	13	-		
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	13	-		
Turn - off Delay Time	t _{d(off)}	$T_j = 25^{\circ}C$	-	33	-	ns	
Fall Time	t _f	Inductive Load	-	95	-		
Turn - on Delay Time	t _{d(on)}	$I_{\rm C} = 8$ A, $V_{\rm CC} = 400$ V	-	13	-		
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	14	-		
Turn - off Delay Time	$t_{d(off)}$	T _j = 175°C	-	50	-	ns	
Fall Time	t _f	Inductive Load	-	120	-		
		$I_{\rm C} = 24$ A, $V_{\rm CC} = 520$ V					
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-	
		R _G = 50Ω, T _j = 175°C					
		$V_{CC} \leq 360V$					
Short Circuit Withstand Time	t _{sc}	V _{GE} = 15V	5	-	-	μs	
		T _j = 25°C					

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	$I_F = 8A$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.4 1.4	1.9 -	V
Diode Reverse Recovery Time	t _{rr}	I _F = 8A	-	42	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs	-	5.2	-	А
Diode Reverse Recovery Charge	Q _{rr}	T _j = 25°C	-	0.12	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 8A	-	116	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs	-	8.1	-	А
Diode Reverse Recovery Charge	Q _{rr}	T _j = 175°C	-	0.51	-	μC

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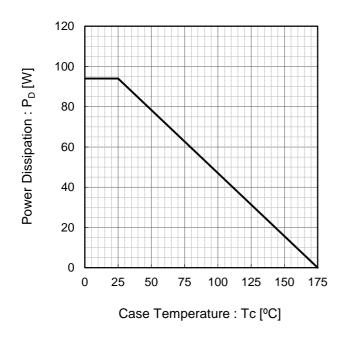


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

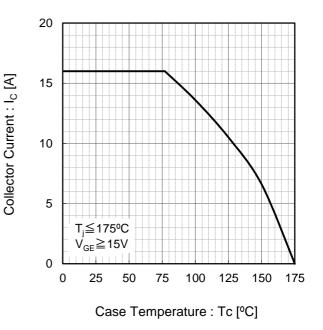
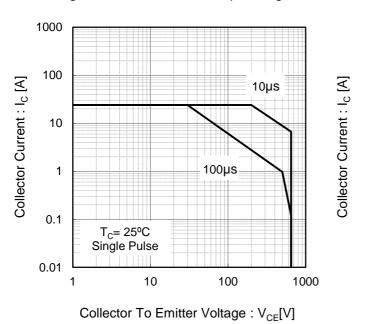
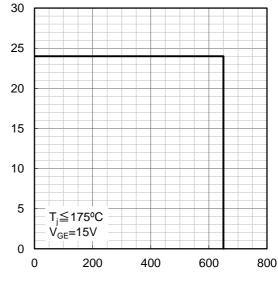


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





Collector To Emitter Voltage : $V_{CE}[V]$

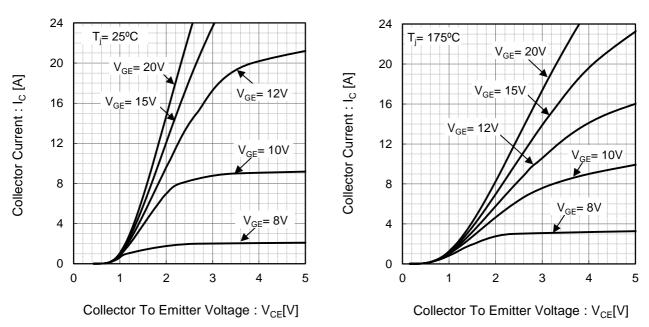


Fig.5 Typical Output Characteristics

Fig.6 Typical Output Characteristics

Fig.7 Typical Transfer Characteristics

Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

I_C= 16A

 $I_{C} = 8A$

 $I_{C} = 4A$

150

175

125

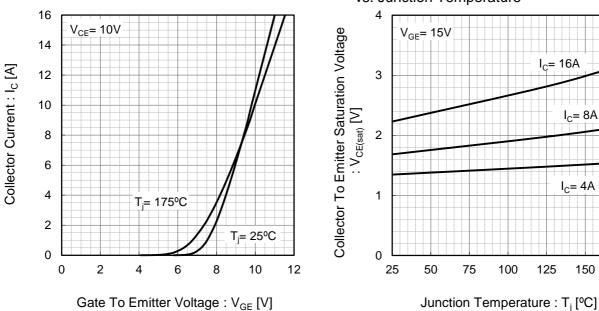


Fig.10 Typical Collector To Emitter Saturation Voltage

•Electrical Characteristic Curves

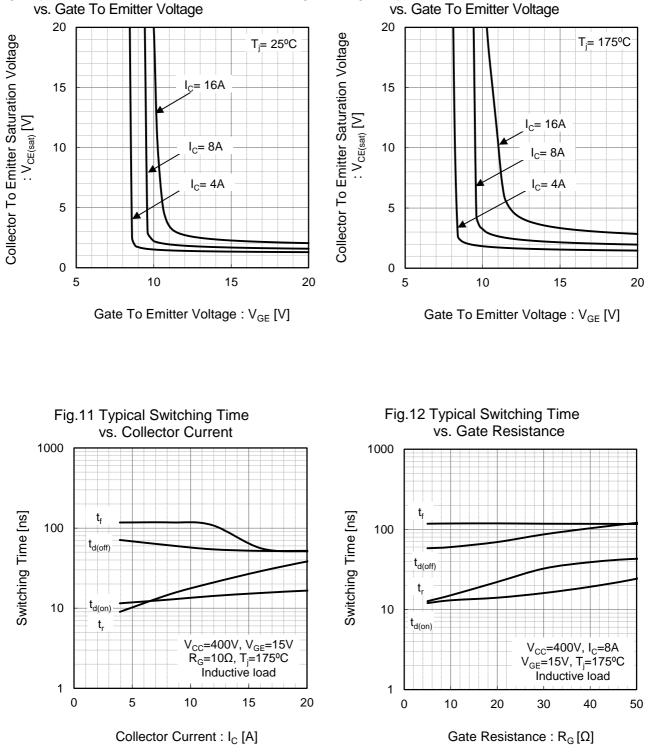
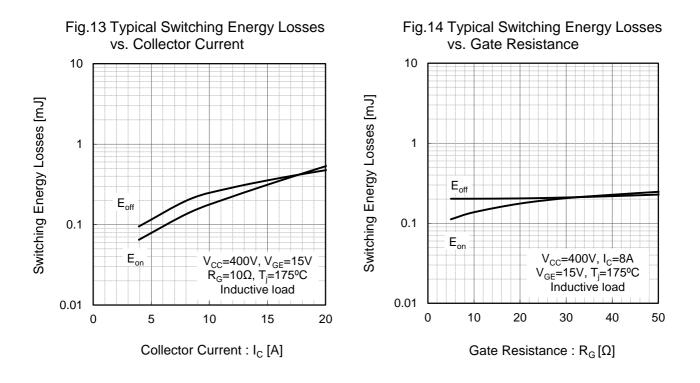
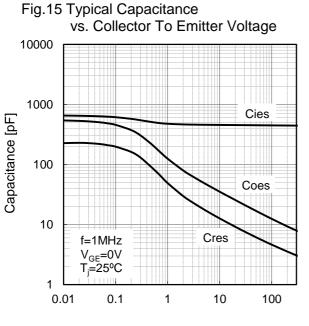


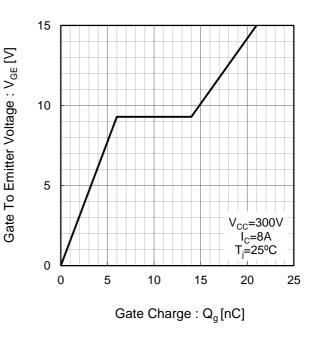
Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage





Collector To Emitter Voltage : V_{CE}[V]

Fig.16 Typical Gate Charge



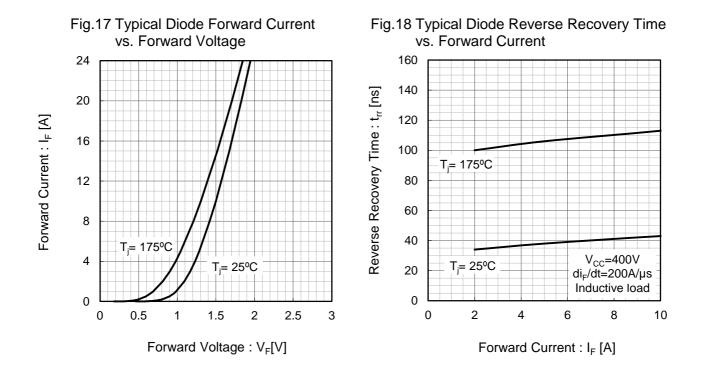
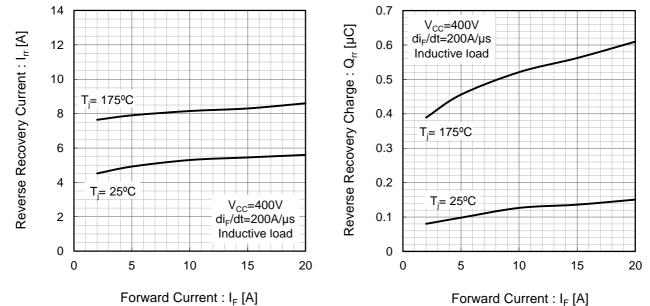


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



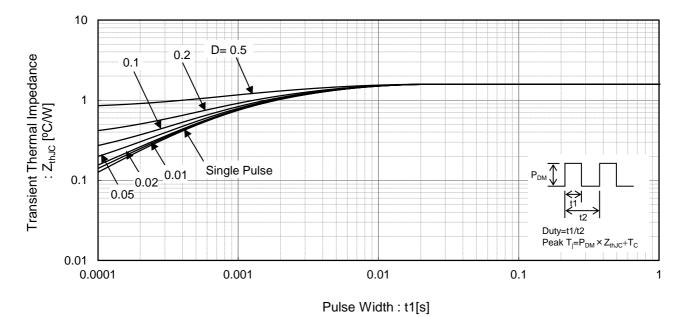


Fig.21 IGBT Transient Thermal Impedance



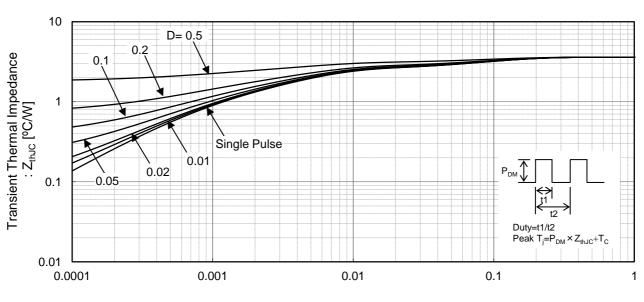


Fig.22 Diode Transient Thermal Impedance

Pulse Width : t1[s]

●Inductive Load Switching Circuit and Waveform

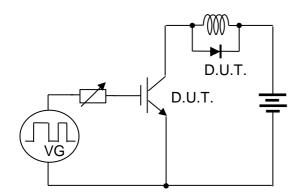


Fig.23 Inductive Load Circuit

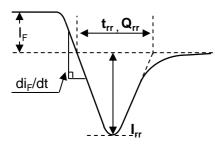


Fig.25 Diode Reverce Recovery Waveform

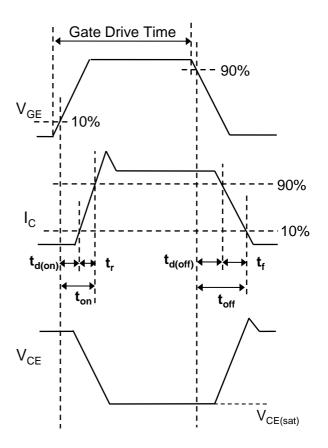


Fig.24 Inductive Load Waveform



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