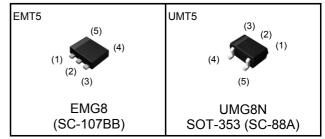
# EMG8 / UMG8N

NPN 100mA 50V Complex Digital Transistors (Bias Resistor Built-in Transistors)

**Datasheet** 

Parameter	Tr1 and Tr2
V <sub>CC</sub>	50V
I <sub>C(MAX.)</sub>	100mA
R <sub>1</sub>	$4.7$ k $\Omega$
$R_2$	47kΩ

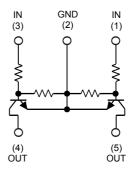
## Outline



#### Features

- 1) Built-In Biasing Resistors.
- 2) Two DTC143Z chips in one package.
- 3) Emitter(GND)-common type.
- 4) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 5) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the circuit design easy.
- 7) Lead Free/RoHS Compliant.

#### •Inner circuit



## Application

Inverter circuit, Interface circuit, Driver circuit

#### Packaging specifications

Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
EMG8	EMT5	1616	T2R	180	8	8,000	G8
UMG8N	UMT5	2021	TR	180	8	3,000	G8

## ● Absolute maximum ratings (Ta = 25°C)

<For Tr1 and Tr2 in common>

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>IN</sub>	−5 to +30	V
Output current	Io	100	mA
Collector current	I <sub>C(MAX.)</sub> *1	100	mA
Power dissipation	P <sub>D</sub> *2	150 (Total) <sup>*3</sup>	mW
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	−55 to +150	°C

## ●Electrical characteristics(Ta = 25°C)

<For Tr1 and Tr2 in common>

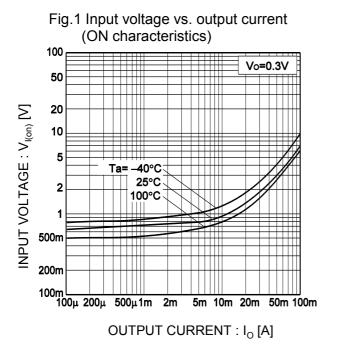
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input voltage	$V_{I(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	ı	ı	0.5	V
input voitage	$V_{I(on)}$	$V_0 = 0.3V, I_0 = 5mA$	1.3	-	-	V
Output voltage	$V_{O(on)}$	I <sub>O</sub> / I <sub>I</sub> = 5mA / 0.25mA	-	0.1	0.3	V
Input current	I <sub>I</sub>	V <sub>I</sub> = 5V	-	-	1.8	mA
Output current	I <sub>O(off)</sub>	$V_{CC} = 50V, V_{I} = 0V$	-	-	0.5	μΑ
DC current gain	G <sub>I</sub>	$V_0 = 5V, I_0 = 10mA$	80	-	-	-
Input resistance	R <sub>1</sub>	-	3.29	4.7	6.11	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	8	10	12	-
Transition frequency	f <sub>T</sub> *1	$V_{CE} = 10V, I_{E} = -5mA,$ f = 100MHz	-	250	-	MHz

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference footprint

<sup>\*3 120</sup>mW per element must not be exceeded.

## ●Electrical characteristic curves(Ta = 25°C)



(OFF characteristics) 10m Vcc=5V 5m 2m OUTPUT CURRENT : Io [A] 1m **500**μ 200μ Ta=100°C 100μ 25°C -40°C 50μ 20μ 10μ 5μ 1μ 1.5 2.0 3.0 INPUT VOLTAGE :  $V_{I(off)}[V]$ 

Fig.2 Output current vs. input voltage

Fig.3 Output current vs. output voltage

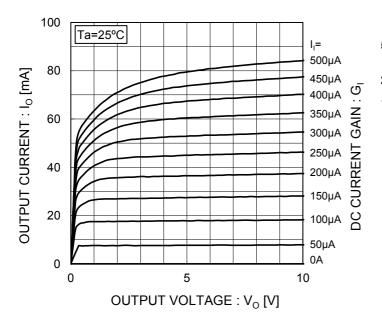
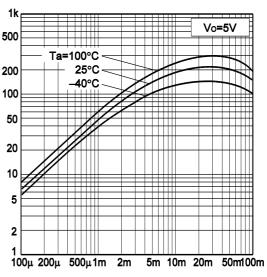
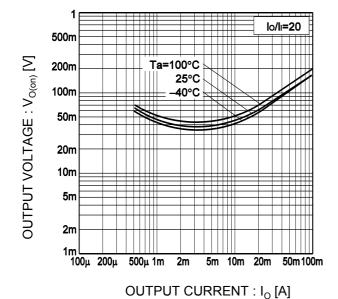


Fig.4 DC current gain vs. output current

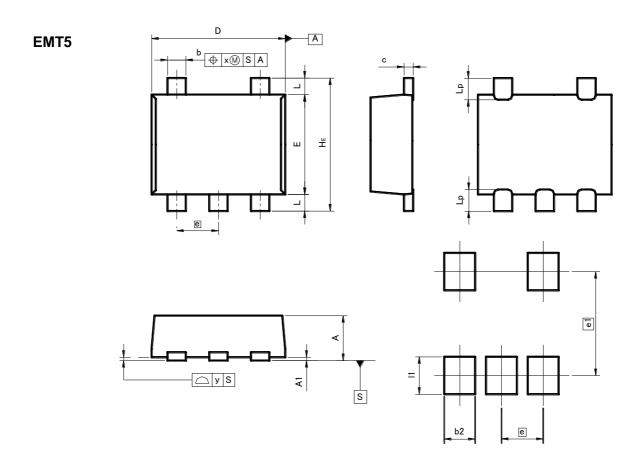


## ●Electrical characteristic curves(Ta = 25°C)

Fig.5 Output voltage vs. output current



## ●Dimensions (Unit : mm)



## Patterm of terminal position areas

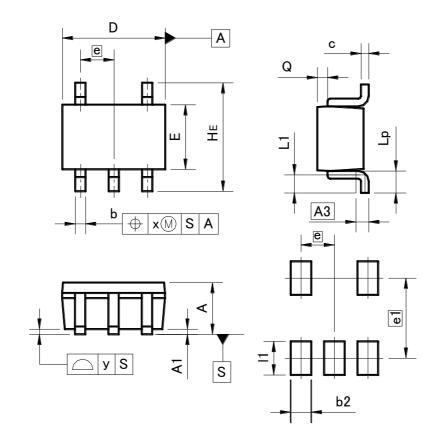
DIM	MILIMI	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.10	0	0.004
Α	0.45	0.55	0.018	0.022
b	0.17	0.27	0.007	0.011
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
Е	1.10	1.30	0.043	0.051
е	0.50		0.02	
HE	1.50	1.70	0.059	0.067
L	0.10	0.30	0.004	0.012
Lp	-	0.35	_	0.014
Х	_	0.10	_	0.004
У	_	0.10		0.004

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
e1	1.25		0.049	
b2	- 0.37		ı	0.015
11	- 0.45			0.018

Dimension in mm/inches

## ●Dimensions (Unit : mm)

## UMT5



## **Patterm of terminal position areas**

DIM	MILIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0	0.004
A3	0.3	25	0.0	01
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.65		0.03	
HE	2.00	2.20	0.079	0.087
L1	0.20	0.50	0.008	0.02
Lp	0.25	0.55	0.01	0.022
Q	0.10	0.30	0.004	0.012
х	_	0.10		0.004
У	_	0.10	_	0.004

DIM	MILIM	MILIMETERS		HES	
DIM	MIN	MAX	MIN	MAX	
e1	1.55		0.06		
b2	- 0.40		-	0.016	
11	- 0.65		-	0.026	

Dimension in mm/inches

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