# **4V Drive Nch MOSFET**

RSF015N06 Datasheet

#### Structure

Silicon N-channel MOSFET

#### ● Features

- 1) Built-in G-S Protection Diode.
- 2) Small Surface Mount Package (TUMT3).
- 3) Low voltage drive. (4V)

### Application

Switching

#### Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	3000
RSF015N0	0	

#### ◆Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		$V_{DSS}$	60	V
Gate-source voltage		V <sub>GSS</sub> *1	±20	V
Drain current	Continuous	I <sub>D</sub>	±1.5	Α
	Pulsed	I <sub>DP</sub> *1	±6.0	Α
Source current	Continuous	I <sub>S</sub>	0.6	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	6.0	Α
Power dissipation	P <sub>D</sub> *2	0.8	W	
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

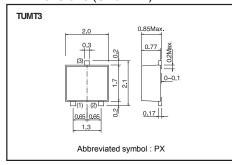
<sup>\*1</sup> Pw≤10μs, Duty cycle≤1%

#### ●Thermal resistance

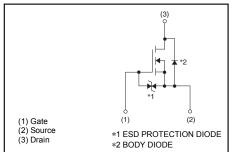
Parameter	Symbol	Limits	Unit
Channel to Ambient	Rth (ch-a)*	156	°C / W

<sup>\*</sup>Mounted on a ceramic board.

#### ●Dimensions (Unit: mm)



#### •Inner circuit



<sup>\*2</sup> Mounted on a ceramic board.

## ●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±10	μΑ	$V_{GS}$ =±20V, $V_{DS}$ =0V
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	1	-	٧	I <sub>D</sub> =1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>	1	1	1	μΑ	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	1	2.5	٧	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA
Otatia dualin assuma an atata		1	210	290		I <sub>D</sub> =1.5A, V <sub>GS</sub> =10V
Static drain-source on-state resistance	R <sub>DS (on)</sub>	1	240	330	mΩ	I <sub>D</sub> =1.5A, V <sub>GS</sub> =4.5V
redictarioe		-	255	350		I <sub>D</sub> =1.5A, V <sub>GS</sub> =4.0V
Forward transfer admittance	I Y <sub>fs</sub> I*	1.0	-	-	S	I <sub>D</sub> =1.5A, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	-	110	-	pF	V <sub>DS</sub> =10V
Output capacitance	C <sub>oss</sub>	-	28	-	pF	V <sub>GS</sub> =0V
Reverse transfer capacitance	C <sub>rss</sub>	-	12	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	1	6	-	ns	I <sub>D</sub> =0.7A, V <sub>DD</sub> ≒30V
Rise time	t <sub>r</sub> *	1	9	-	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	-	15	-	ns	$R_L$ =42.8 $\Omega$
Fall time	t <sub>f</sub> *	-	10	-	ns	$R_G$ =10 $\Omega$
Total gate charge	Q <sub>g</sub> *	-	2.0	-	nC	I <sub>D</sub> =1.5A
Gate-source charge	Q <sub>gs</sub> *	-	0.8	-	nC	V <sub>DD</sub> ≒30V
Gate-drain charge	Q <sub>gd</sub> *	-	0.5	-	nC	V <sub>GS</sub> =5V

<sup>\*</sup>Pulsed

## ●Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.2	V	I <sub>s</sub> =1.5A, V <sub>GS</sub> =0V

<sup>\*</sup>Pulsed

#### ●Electrical characteristic curves (Ta=25°C)

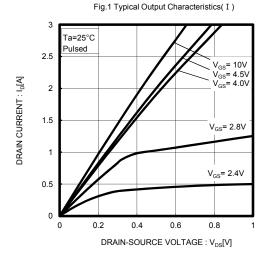


Fig.3 Typical Transfer Characteristics

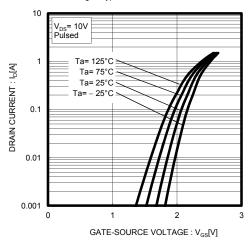


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current( II )

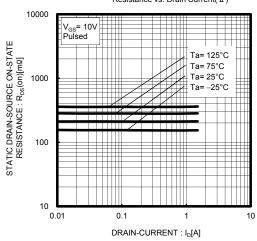


Fig.2 Typical Output Characteristics(II)

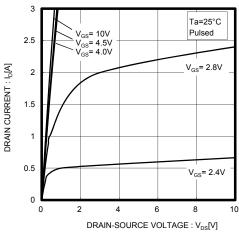


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current( I )

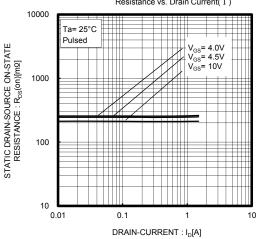
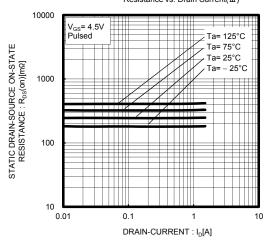
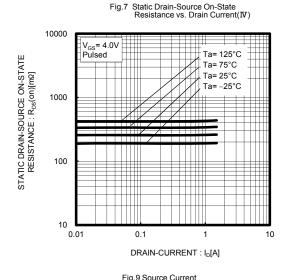
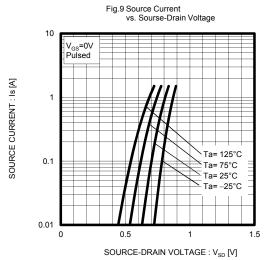
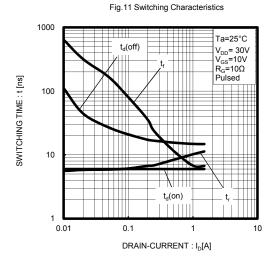


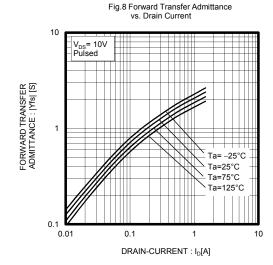
Fig.6 Static Drain-Source On-State Resistance vs. Drain Current(Ⅲ)

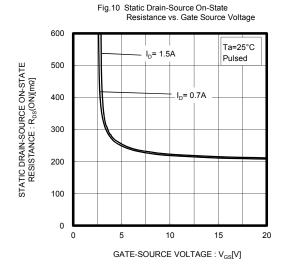












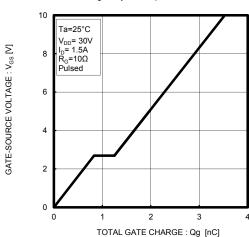
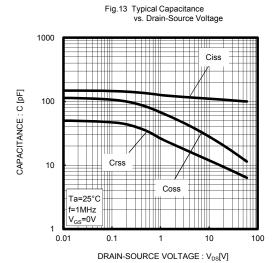


Fig.12 Dynamic Input Characteristics



#### ● Measurement circuits

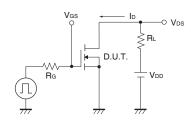


Fig.1-1 Switching Time Measurement Circuit

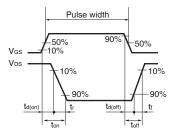


Fig.1-2 Switching Waveforms

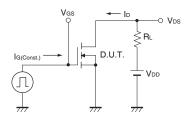


Fig.2-1 Gate Charge Measurement Circuit

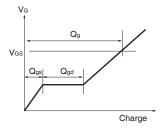


Fig.2-2 Gate Charge Waveform

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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