# 2.5V Drive Pch MOS FET

# RTR030P02

#### Structure

Silicon P-channel MOS FET

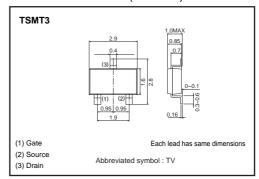
#### ● Features

- 1) Low On-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).

#### Application

Power switching, DC / DC converter.

## ●External dimensions (Unit : mm)



### Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	3000
RTR030P02		0

## ● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	-20	V
Gate-source voltage		V <sub>GSS</sub>	±12	V
Drain aurrent	Continuous	ID	±3.0	Α
Drain current	Pulsed	I <sub>DP</sub> *1	±12	Α
Source current (Body diode)	Continuous	Is	-0.8	Α
	Pulsed	I <sub>SP</sub> *1	-3.2	Α
Total power dissipation		P <sub>D</sub> *2	1.0	W
Channel temperature		Tch	150	°C
Range of Storage temperature		Tstg	-55 to +150	°C
			•	

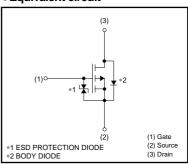
<sup>\*1</sup> Pw≤10μs, Duty cycle≤1% \*2 Mounted on a ceramic board

#### Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	Rth (ch-a)*	125	°C / W

#### \* Mounted on a ceramic board.

#### ●Equivalent circuit



# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs=±12V, Vps=0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	-20	_	_	٧	I <sub>D</sub> = -1mA, V <sub>G</sub> S=0V
Zero gate voltage drain current	IDSS	-	_	-1	μΑ	V <sub>DS</sub> = -20V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	-0.7	_	-2.0	٧	$V_{DS}$ = -10V, $I_D$ = -1mA
Static drain-source on-state resistance		_	55	75	$m\Omega$	I <sub>D</sub> = -3.0A, V <sub>G</sub> S= -4.5V
	R <sub>DS (on)</sub> *	_	60	85	$m\Omega$	I <sub>D</sub> = -3.0A, V <sub>G</sub> S= -4.0V
		_	90	125	$m\Omega$	I <sub>D</sub> = -1.5A, V <sub>G</sub> S= -2.5V
Forward transfer admittance	Y <sub>fs</sub>   *	2.5	_	_	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -1.5A
Input capacitance	Ciss	_	840	_	pF	V <sub>DS</sub> = -10V
Output capacitance	Coss	_	140	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	100	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	_	12	_	ns	I <sub>D</sub> = -1.5A
Rise time	tr *	-	20	_	ns	V <sub>DD</sub> = −15V
Turn-off delay time	td (off) *	_	50	_	ns	V <sub>GS</sub> = -4.5V R <sub>L</sub> =10Ω
Fall time	t <sub>f</sub> *	-	20	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg	-	9.3	_	nC	V <sub>DD</sub> ≒−15V
Gate-source charge	Qgs	_	1.6	_	nC	V <sub>GS</sub> = -4.5V
Gate-drain charge	$Q_{gd}$	_	2.6	_	nC	I <sub>D</sub> = -3.0A

<sup>\*</sup>Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	V <sub>SD</sub>	-	_	-1.2	V	I <sub>S</sub> = -0.8A, V <sub>GS</sub> =0V

#### Electrical characteristic curves

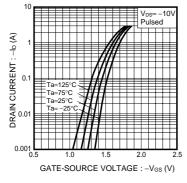


Fig.1 Typical Transfer Characteristics

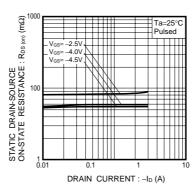


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

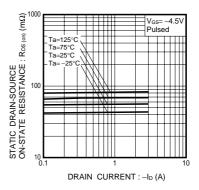


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

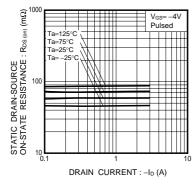


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

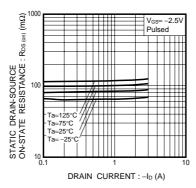


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

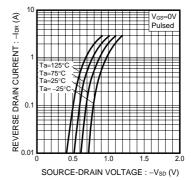


Fig.6 Reverse Drain Current vs.Source-Drain Voltage

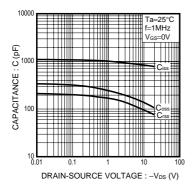


Fig.7 Typical Capacitance vs. Drain-Source Voltage

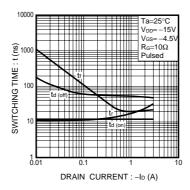


Fig.8 Switching Characteristics

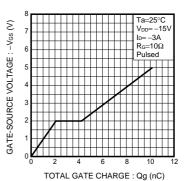


Fig.9 Dynamic Input Characteristics

#### ●Measurement circuits

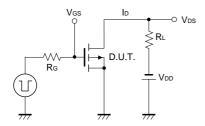


Fig.10 Switching Time Test Circuit

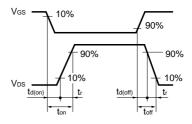


Fig.11 Switching Time Waveforms

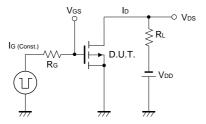


Fig.12 Gate Charge Test Circuit

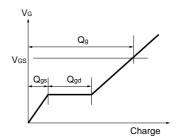


Fig.13 Gate Charge Waveform

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