

# **BTW68**

## 30 A SCRs

### Features

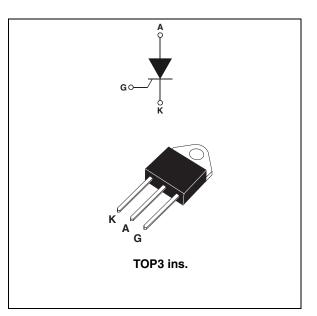
- On-state rms current: 30 A
- Blocking voltage: up to 1200 V
- Gate current: 50 mA
- UL 2500 V insulation (file ref E81734)

### Description

Available in a high power insulated package, the BTW68 series is suitable for applications where power handling and power dissipation are critical such as solid state relays, welding equipment and high power motor control.

Based on a clip assembly technology, this device offers a superior performance in surge current handling capabilities.

Thanks to the internal ceramic pad, the device provides high voltage insulation (2500  $V_{RMS)}$  and complies with UL standards (file ref: E81734).



### Table 1. Device summary

Symbol Value	
I <sub>T(RMS)</sub>	30 A
V <sub>DRM</sub> /V <sub>RRM</sub>	600 to 1200 V
I <sub>GT</sub>	50 mA

# 1 Characteristics

	<b>U</b> (	•	,			
Symbol	Parameter			Value	Unit	
I <sub>T(RMS)</sub>	On-state current rms (180° conduction	n angle)	T <sub>c</sub> = 80 °C	30	А	
IT <sub>(AV)</sub>	Average on-state current (180° conduction	ction angle)	T <sub>c</sub> = 80 °C	19	А	
1	Non repetitive surge peak on-state	t <sub>p</sub> = 8.3 ms	T 05 °C	420	А	
ITSM current	current	t <sub>p</sub> = 10 ms	– T <sub>j</sub> = 25 °C	400		
l <sup>2</sup> t	$I^{2}t$ Value for fusing $T_{i} = 25 \text{ °C}$			800	A <sup>2</sup> s	
dl/dt	Critical rate of rise of on-state current $I_G$ = 2 x $I_{GT}$ , $t_r$ $\leq$ 100 ns	F = 60 Hz	T <sub>j</sub> = 125 °C	100	A/µs	
I <sub>GM</sub>	Peak gate current $t_p = 20 \ \mu s$		T <sub>j</sub> = 125 °C	8	А	
P <sub>G(AV)</sub>	Average gate power dissipation $T_j = 125 \text{ °C}$			1	W	
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C	
V <sub>RGM</sub>	Maximum peak reverse gate voltage			5	V	

### Table 2. Absolute maximum ratings (limiting values)

### Table 3.Electrical characteristics ( $T_j = 25$ °C, unless otherwise specified)

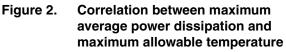
Symbol	٢	Value	Unit			
I <sub>GT</sub>	$V_{\rm D} = 12 \text{ V}, \text{ R}_{\rm I} = 33 \Omega$			MIN.	50	mA
V <sub>GT</sub>	$v_{\rm D} = 12 v, n_{\rm L} = 33.32$			MAX.	1.5	V
V <sub>GD</sub>	$V_D = V_{DRM,} R_L = 3.3 \text{ k}\Omega$		T <sub>j</sub> = 125 °C	MIN.	0.2	V
t <sub>gt</sub>	$V_D = V_{DRM}$ , $I_G = 200 \text{ mA}$ ,	dl <sub>G</sub> /dt = 1.5 A/µs	6	TYP.	2	μs
I <sub>H</sub>	I <sub>T</sub> = 500 mA, gate open	I <sub>T</sub> = 500 mA, gate open			75	mA
١ <sub>L</sub>	$I_{G} = 1.2 \text{ x } I_{GT}$			TYP.	40	mA
dV/dt	V <sub>D</sub> = 67 % V <sub>DRM</sub>	V <sub>DRM</sub> = 800 V	T <sub>j</sub> = 125 °C	5 °C MIN.	500	V/µs
uv/ut	gate open	V <sub>DRM</sub> = 1000 V	$r_j = 125 \ \text{O}$		250	
V <sub>TM</sub>	I <sub>TM</sub> = 60 A, t <sub>p</sub> = 380 μs			MAX.	2.1	V
I <sub>DRM</sub>			T <sub>j</sub> = 25 °C	MAX.	20	μA
I <sub>RRM</sub>	$V_{DRM} = V_{RRM}$		T <sub>j</sub> = 125 °C		6	mA
t <sub>q</sub>	$V_D = 67\% V_{DRM}, I_{TM} = 60 \text{ A}, V_R = 75 \text{ V}$ $dI_{TM}/dt = 30 \text{ A}/\mu\text{s}, dV_D/dt = 20 \text{ V}/\mu\text{s}$		T <sub>j</sub> = 125 °C	TYP.	100	μs

### Table 4. Thermal resistance

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (D.C.)	1.1	°C/W
R <sub>th(j-a)</sub>	Junction to ambient	50	°C/W



#### Figure 1. Maximum average power dissipation versus average on-state current



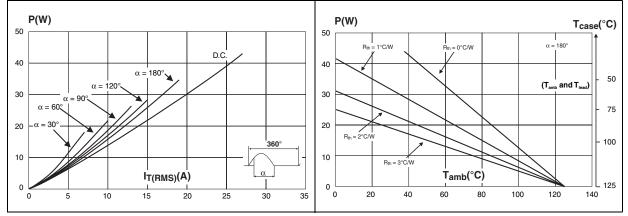


Figure 3. Average on-state current versus case Figure 4. temperature

Relative variation of thermal impedance versus pulse duration

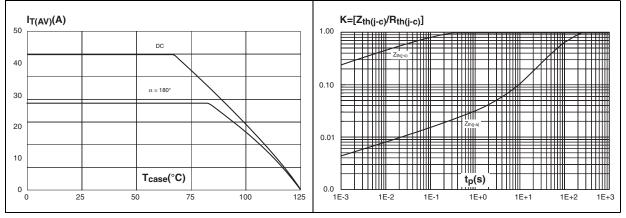
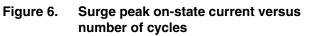
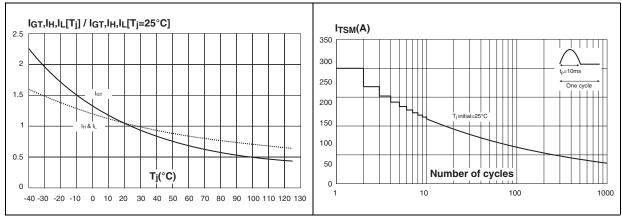


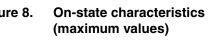
Figure 5. Relative variation of gate trigger current versus junction temperature

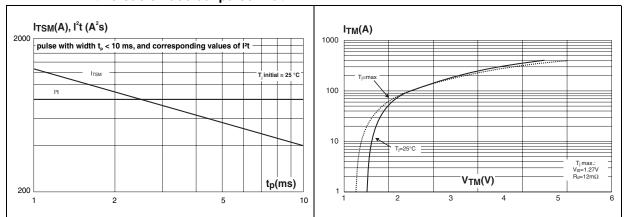




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## Figure 7. Non repetitive surge peak on-state Figure 8. current and corresponding value of I<sup>2</sup>t versus sinusoidal pulse width







# 2 Ordering information scheme

Figure 9.	Orderina	information	scheme
	•••••		

	BTW 68 - 600 RG
Standard SCR series	
Туре	
68 = 30A	
Voltage	
600 = 600V	
800 = 800V	
1000 = 100V	
1200 = 1200V	
Packing mode	
RG = Tube	

### Table 5. Product Selector

Part numbers		Voltage (xxx) Sensitivity Pac		Package		
	600 V	800 V	1000 V	1200 V		
BTW68-600RG	Х					TOP3 Ins.
BTW68-800RG		Х			- 50 mA	
BTW68-1000RG			Х			1053 115.
BTW68-1200RG				Х		

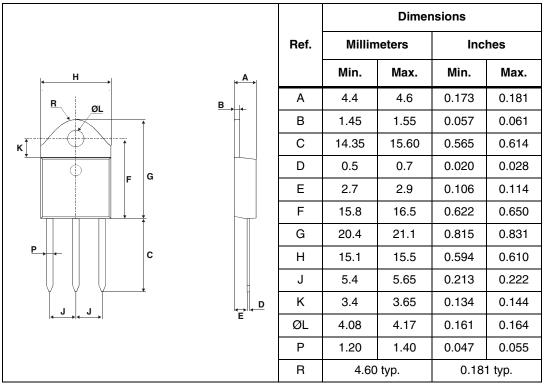


## 3 Package information

- Epoxy meets UL94,V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK<sup>®</sup> is an ST trademark.

Table 6. TOP3 ins. dimensions





## 4 Ordering information

### Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
BTW68-600RG	BTW68-600				
BTW68-800RG	BTW68-800	TOP3 ins.	4.5 g	30	Tube
BTW68-1000RG	BTW68-1000	1053 115.	4.5 y	30	Tube
BTW68-1200RG	BTW68-1200				

## 5 Revision history

#### Table 8. Document revision history

Date	Revision	Changes
Mar-1995	1	Initial release.
13-Feb-2006	2	TOP3 Insulated delivery mode changed from bulk to tube. ECOPACK statement added.
29-Jul-2010	3	Deleted part number BTW68-200RG. Updated <i>Table 2</i> , <i>Figure 7</i> and alpha angle in <i>Figure 1</i> .



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