

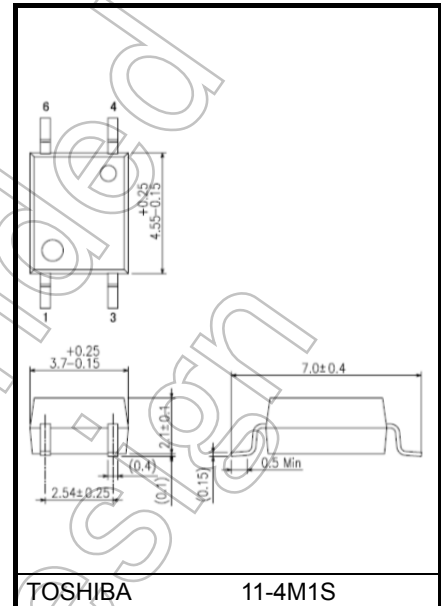
TLP184

Telephone Use Equipment
 Programmable Controllers
 AC / DC-Input Module
 Telecommunication

Unit: mm

The TOSHIBA mini flat coupler TLP184 is a small outline coupler, suitable for surface mount assembly.
 TLP184 consist of a photo transistor, optically coupled to two gallium arsenide infrared emitting diodes connected inverse parallel, and can operate directly by AC input current.

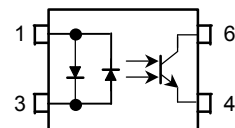
- Collector-emitter voltage: 80V (min)
- Current transfer ratio: 50% (min)
 Rank GB: 100% (min)
- Isolation voltage: 3750 Vrms (min)
- Operation Temperature: -55 to 110 °C
- Safety Standards
 UL approved: UL1577, File No. E67349
 cUL approved: CSA Component Acceptance Service No. 5A
 File No.E67349
- BSI under application: BS EN60065:2002, Certificate No. 9020
 BS EN60950-1:2006, Certificate No. 9021
- Option (V4) type
 VDE approved: EN60747-5-5, Certificate No. 40009347
 (Note): When a EN60747-5-5 approved type is needed,
 Please designate "Option(V4)"
- Construction mechanical rating
 Creepage distance : 5.0 mm (min)
 Clearance : 5.0 mm (min)
 Insulation thickness : 0.4 mm (min)



TOSHIBA 11-4M1S

Weight: 0.08 g (typ.)

Pin Configuration (top view)



- 1: Anode, Cathode
- 3: Cathode, Anode
- 4: Emitter
- 6: Collector

Start of commercial production
 2011/12

Current Transfer Ratio

| Type | Classification(*1) | Current Transfer Ratio (%) (I_C/I_F) | | Marking of classification |
|--------|--------------------|--|-----|---------------------------|
| | | $I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}, T_a = 25^\circ\text{C}$ | | |
| | | Min | Max | |
| TLP184 | Standard | 50 | 400 | Blank, YE, GR, B, GB |
| | Rank Y | 50 | 150 | YE |
| | Rank GR | 100 | 300 | GR |
| | Rank BLL | 200 | 400 | B |
| | Rank GB | 100 | 400 | GB |

(Note1): ex. rank GB: TLP184 (GB,E)

(Note) Application type name for certification test, please use standard product type name, i.e.
TLP184(GB,E: TLP184

Not Recommended for New Design

Absolute Maximum Ratings (Ta = 25°C)

| Characteristic | | Symbol | Rating | Unit |
|---|--|-------------------------|------------|---------|
| LED | R.M.S. forward current | $I_{F(RMS)}$ | ±50 | mA |
| | Forward current derating (Ta≥90°C) | $\Delta I_F / ^\circ C$ | -1.5 | mA / °C |
| | Pulse forward current (Note 2) | I_{FP} | ±1 | A |
| | Junction temperature | T_j | 125 | °C |
| Detector | Collector-emitter voltage | V_{CEO} | 80 | V |
| | Emitter-collector voltage | V_{ECO} | 7 | V |
| | Collector current | I_C | 50 | mA |
| | Power dissipation | P_C | 150 | mW |
| | Power dissipation derating (Ta ≥ 25°C) | $\Delta P_C / ^\circ C$ | -1.5 | mW / °C |
| | Junction temperature | T_j | 125 | °C |
| Operating temperature range | | T_{opr} | -55 to 110 | °C |
| Storage temperature range | | T_{stg} | -55 to 125 | °C |
| Lead soldering temperature (10 s) | | T_{sol} | 260 | °C |
| Total package power dissipation | | P_T | 200 | mW |
| Total package power dissipation derating (Ta ≥ 25°C) | | $\Delta P_T / ^\circ C$ | -2.0 | mW / °C |
| Isolation voltage (AC, 1 minute, R.H. ≤ 60%) (Note 3) | | BV_S | 3750 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: Pulse width ≤ 100 μs, f=100 Hz

Note 3: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

Recommended Operating Conditions (Note)

| Characteristic | Symbol | Min | Typ. | Max | Unit |
|-------------------|--------------|-----|------|-----|------|
| Supply voltage | V_{CC} | — | 5 | 48 | V |
| Forward current | $I_{F(RMS)}$ | — | 16 | 20 | mA |
| Collector current | I_C | — | 1 | 10 | mA |

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

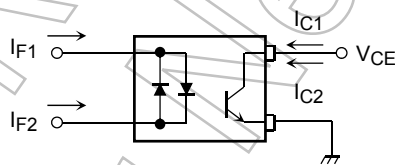
Electrical Characteristics (Ta = 25°C)

| Characteristic | | Symbol | Test Condition | Min | Typ. | Max | Unit |
|------------------------------------|-------------------------------------|----------------|---|-----|------|------|---------------|
| LED | Forward voltage | V_F | $I_F = \pm 10 \text{ mA}$ | 1.1 | 1.25 | 1.4 | V |
| | Capacitance | C_T | $V = 0, f = 1 \text{ MHz}$ | — | 60 | — | pF |
| Detector | Collector-emitter breakdown voltage | $V_{(BR) CEO}$ | $I_C = 0.5 \text{ mA}$ | 80 | — | — | V |
| | Emitter-collector breakdown voltage | $V_{(BR) ECO}$ | $I_E = 0.1 \text{ mA}$ | 7 | — | — | V |
| | Collector dark current | I_{CEO} | $V_{CE} = 48 \text{ V}$ | — | 0.01 | 0.08 | μA |
| | | | $V_{CE} = 48 \text{ V}, T_a = 85^\circ\text{C}$ | — | 2 | 50 | μA |
| Capacitance (collector to emitter) | | C_{CE} | $V = 0, f = 1 \text{ MHz}$ | — | 10 | — | pF |

Coupled Electrical Characteristics (Ta = 25°C)

| Characteristic | Symbol | Test Condition | Min | Typ. | Max | Unit |
|--------------------------------------|--------------------------|--|------|------|-----|---------------|
| Current transfer ratio | I_C / I_F | $I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$ | 50 | — | 400 | % |
| | | Rank GB | 100 | — | 400 | |
| Saturated CTR | $I_C / I_F (\text{sat})$ | $I_F = \pm 1 \text{ mA}, V_{CE} = 0.4 \text{ V}$ | — | 60 | — | % |
| | | Rank GB | 30 | — | — | |
| Collector-emitter saturation voltage | $V_{CE} (\text{sat})$ | $I_C = 2.4 \text{ mA}, I_F = \pm 8 \text{ mA}$ | — | — | 0.3 | V |
| | | $I_C = 0.2 \text{ mA}, I_F = \pm 1 \text{ mA}$ | — | 0.2 | — | |
| | | Rank GB | — | — | 0.3 | |
| Off-state collector current | $I_{C(\text{off})}$ | $V_F = \pm 0.7 \text{ V}, V_{CE} = 48 \text{ V}$ | — | 1 | 10 | μA |
| CTR symmetry | $I_C (\text{ratio})$ | $I_C (I_F = -5 \text{ mA}) / I_C (I_F = 5 \text{ mA})$ (Note 4) | 0.33 | 1 | 3 | — |

Note 4: $I_C(\text{ratio}) = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5\text{V})}{I_{C1}(I_F = I_{F1}, V_{CE} = 5\text{V})}$



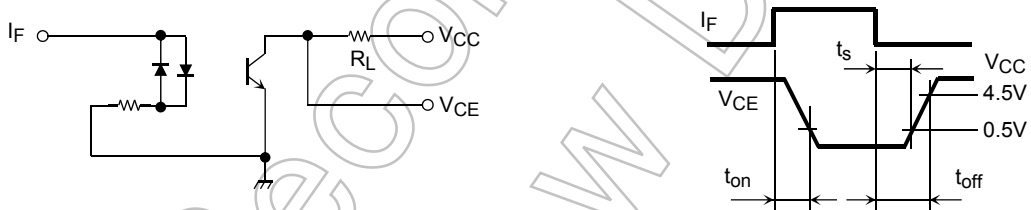
Isolation Characteristics (Ta = 25°C)

| Characteristic | Symbol | Test Condition | Min | Typ. | Max | Unit |
|-----------------------------|-----------------|------------------------------------|--------------------|------------------|-----|------------------|
| Capacitance input to output | C _S | V _S = 0 V, f = 1 MHz | — | 0.8 | — | pF |
| Isolation resistance | R _S | V _S = 500 V, R.H. ≤ 60% | 1×10 ¹⁰ | 10 ¹⁴ | — | Ω |
| Isolation voltage | BV _S | AC, 1 minute | 3750 | — | — | V _{rms} |
| | | AC, 1 second, in oil | — | 10000 | — | |
| | | DC, 1 minute, in oil | — | 10000 | — | V _{dc} |

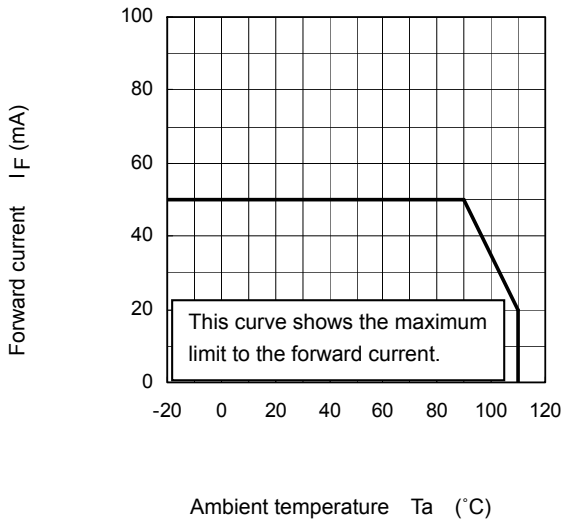
Switching Characteristics (Ta = 25°C)

| Characteristic | Symbol | Test Condition | Min | Typ. | Max | Unit |
|----------------|------------------|--|-----|------|-----|------|
| Rise time | t _r | V _{CC} = 10 V, I _C = 2 mA R _L = 100 Ω | — | 5 | — | μs |
| Fall time | t _f | | — | 9 | — | |
| Turn-on time | t _{on} | | — | 9 | — | |
| Turn-off time | t _{off} | | — | 9 | — | |
| Turn-on time | t _{on} | R _L = 1.9 kΩ (Fig. 1) V _{CC} = 5 V, I _F = ±16 mA | — | 2 | — | μs |
| Storage time | t _s | | — | 30 | — | |
| Turn-off time | t _{off} | | — | 70 | — | |

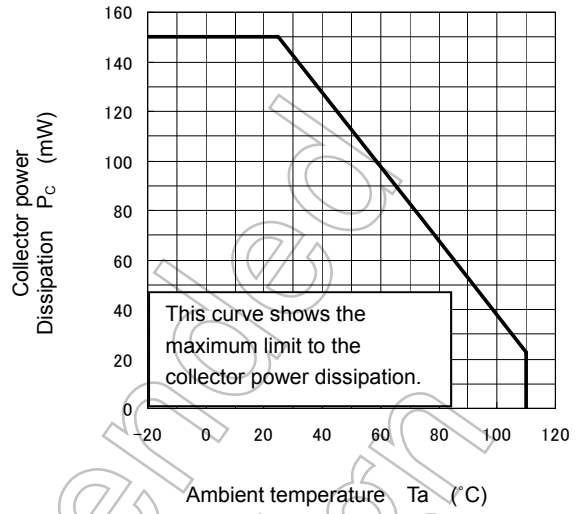
Fig. 1: Switching time test circuit



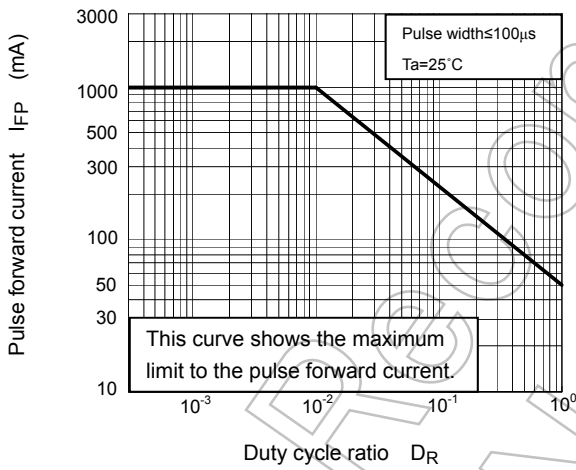
$I_F - T_a$



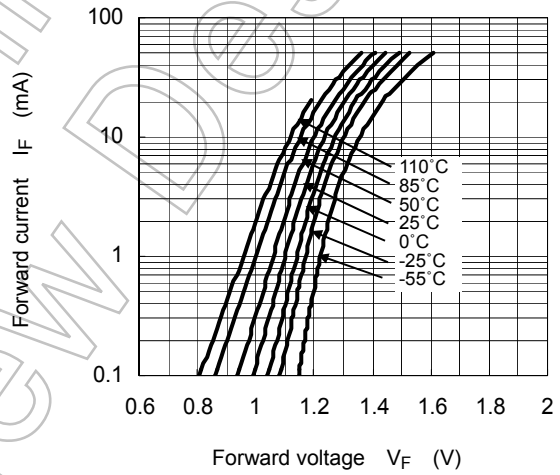
$P_C - T_a$



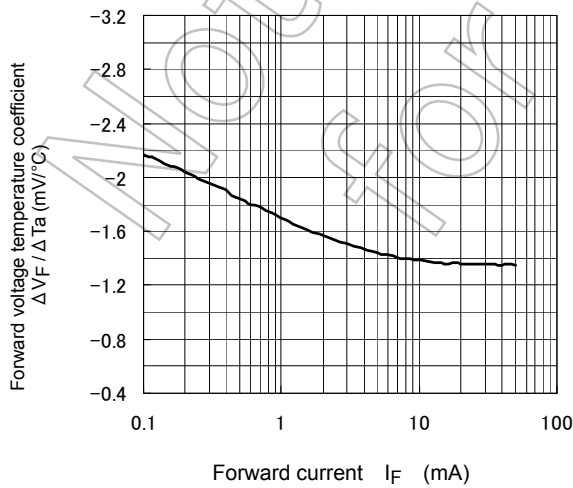
$I_{FP} - D_R$



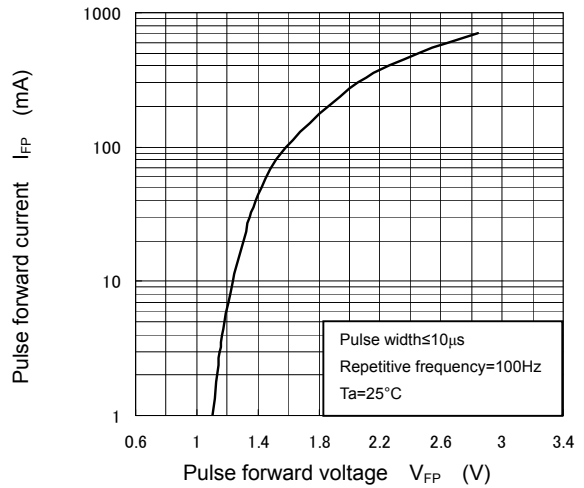
$I_F - V_F$



$\Delta V_F / \Delta T_a - I_F$

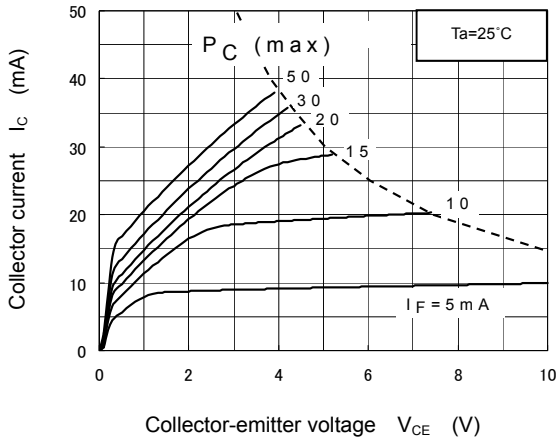


$I_{FP} - V_{FP}$

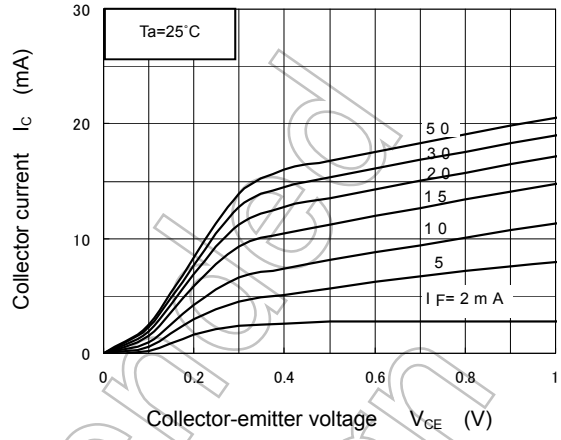


*The above graphs show typical characteristic.

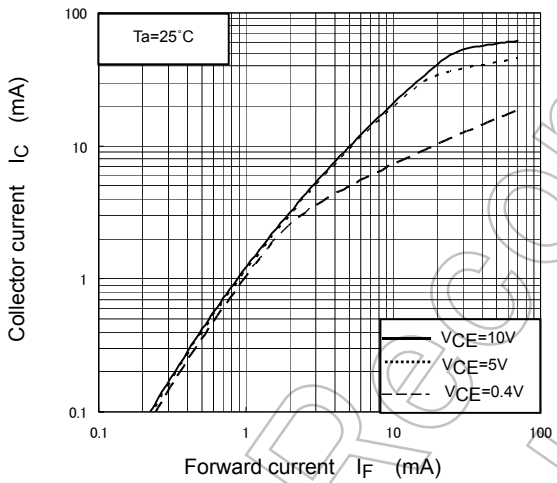
$I_C - V_{CE}$



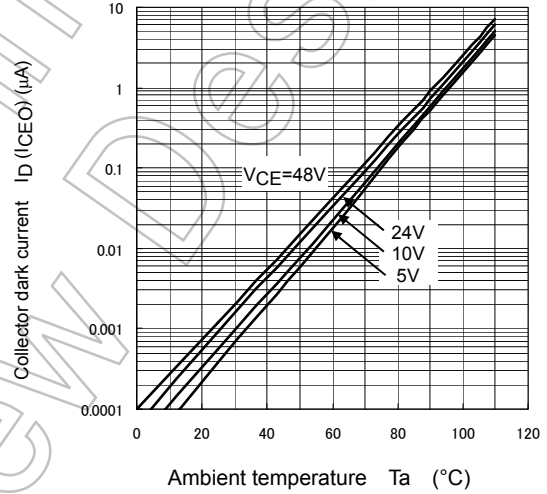
$I_C - V_{CE}$



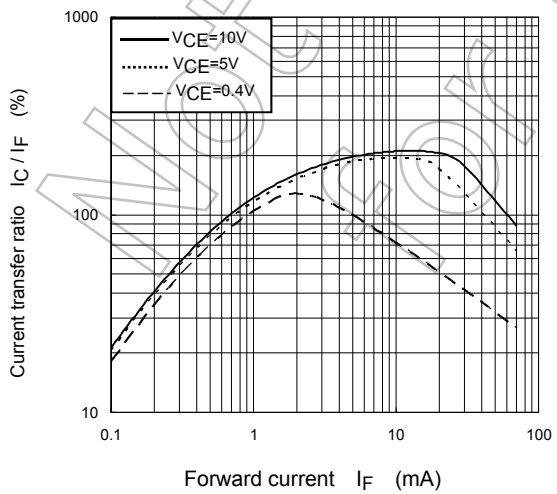
$I_C - I_F$



$I_{CEO} - T_a$

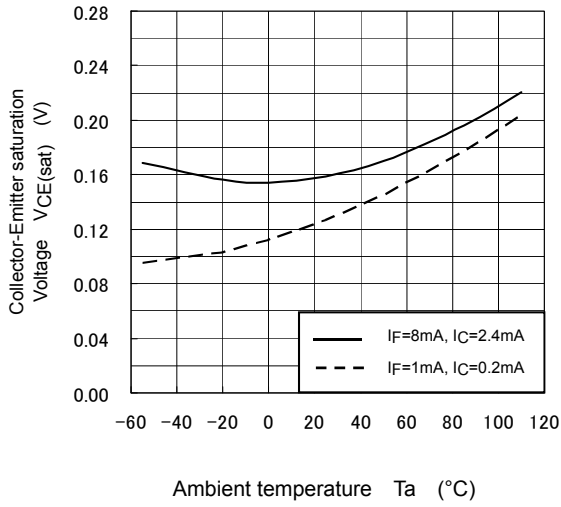


$I_C / I_F - I_F$

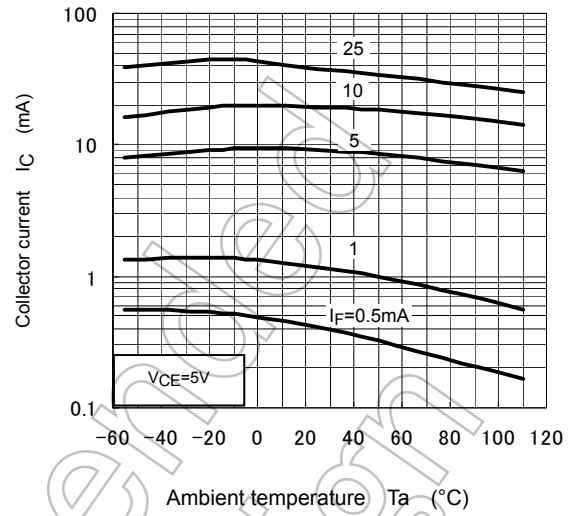


*The above graphs show typical characteristic.

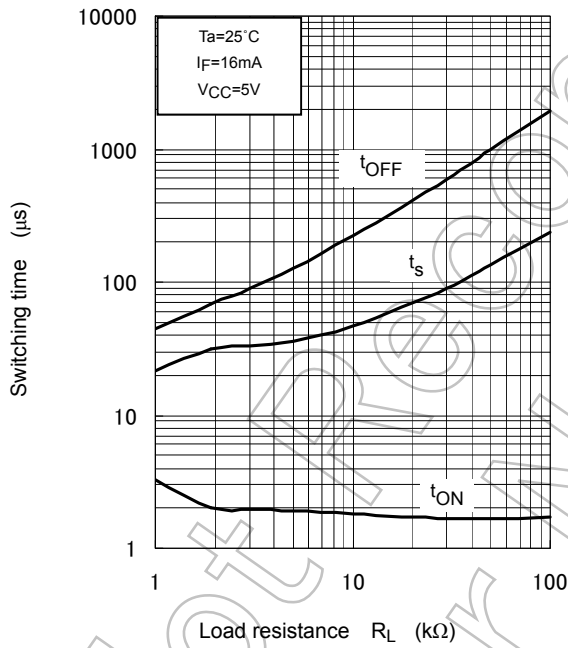
$V_{CE(sat)} - T_a$



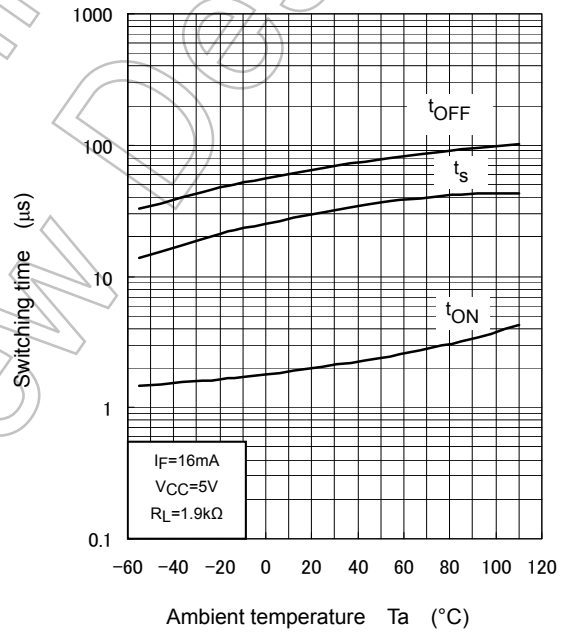
$I_C - T_a$



Switching time - R_L



Switching time - T_a



*The above graphs show typical characteristic.

Soldering and Storage

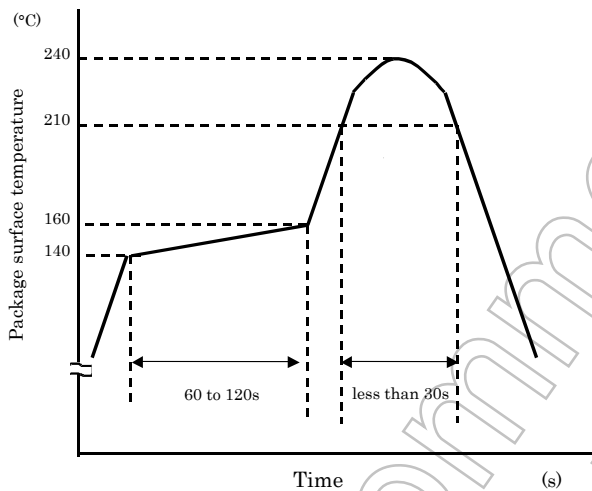
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

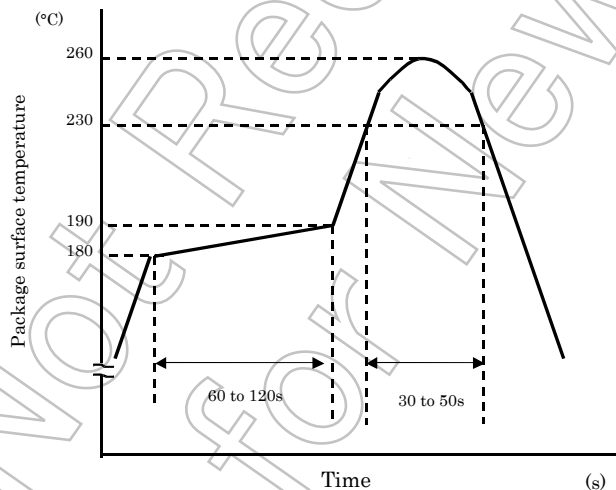
1) Using solder reflow

· Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value. Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

· Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value. Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

Please preheat it at 150°C between 60 and 120 seconds.

Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Not Recommended
for New Design

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